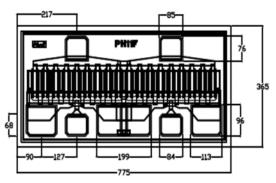




Features:

- 33.0 dBm of Power at 12 GHz
- 12.0 dB Small Signal Gain at 12 GHz
- 45% PAE at 12 GHz
- 0.25 x 2400 Micron Refractory Metal/Gold Gate
- Excellent for Medium Power, Gain, and High Power Added Efficiency
- Ideal for Commercial, Military, Hi-Rel Space Applications



Chip Dimensions: 775 x 365 microns Chip Thickness: 100 microns

Description:

The MwT-PH11F is a AlGaAs/InGaAs PHEMT (Pseudomorphic-High-Electron-Mobility-Transistor) device whose nominal 0.25 micron Gate length and 2400 micron gate width make it ideally suited for applications requiring high-gain and medium power up to 12 GHz frequency range. The device is equally effective for either wideband or narrow-band applications. The chip is produced using reliable metal systems and passivated to insure excellent reliability.

Electrical Specifications: at Ta= 25 °C

PARAMETERS & CONDITIONS	SYMBOL	FREQ	UNITS	MIN	TYP
Output Power at 1dB Compression Vds=8.0V lds=0.7xlDSS	P1dB	12 GHz	dBm		32.0
Saturated Power Vds=8.0V lds=0.7xIDSS	Psat	12 GHz	dBm		33
Output Third Order Intercept Point Vds=8.0V lds=0.7xIDSS	OIP3	12 GHz	dBm		38
Small Signal Gain Vds=8.0V lds=0.7xIDSS	SSG	12 GHz	dB		12.0
Power Added Efficiency at P1dB Vds=8.0V lds=0.7xlDSS	PAE	12 GHz	%		45

Note: Ids should be between 40% and 80% of Idss. Currently, our data shows Ids at 70% of IDSS. Low Ids will improve efficiency, but high Ids will make Psat and IP3 better.

DC Specifications: at Ta= 25 °C

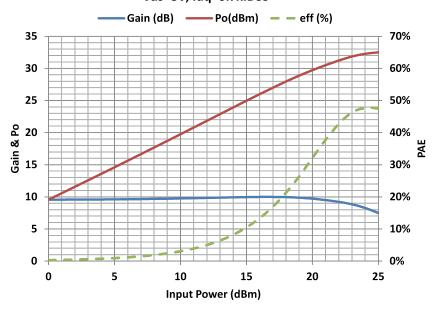
PARAMETERS & 0	CONDITIONS	SYMBOL	UNITS	MIN	TYP	MAX
Saturated Drain Current Vds= 3.0 V Vgs= 0.0 V	IDSS	mA	480		520	
Transconductance Vds= 2.5 V Vgs= 0.0 V		Gm	mS		700	
Pinch-off Voltage Vds= 3.0 V lds= 1.0 mA	Vp	V		-0.8	-1.0	
Gate-to-Source Breakdown Voltage lgs= -0.3 mA		BVGSO	V		-17.0	
Gate-to-Drain Breakdown Voltage lgd= -0.3 mA		BVGDO	V		-18.0	
Chip Thermal Resistance Chip & 71 pkg		Rth	C/W		25	

^{*} Overall Rth depends on case mounting

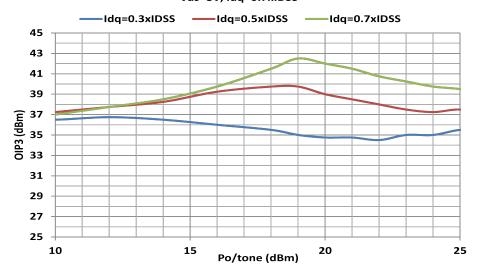




MwT-PH11F, Po, Gain & PAE vs Pin at 12GHz Vds=8V; Idq=0.7xIDSS



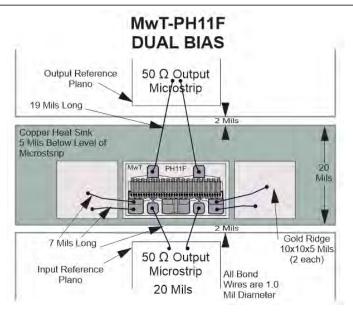
MwT-PH11F, OIP3 at different Idq vs Po/tone at 12GHz Vds=8V; Idq=0.7xIDSS



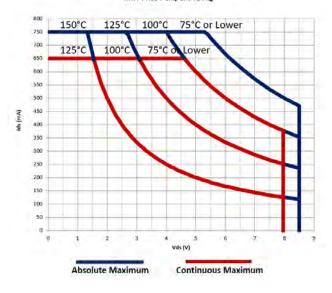




12 GHz Medium Power AlGaAs/InGaAs PHEMT



SAFE OPERATING LIMITS vs BACKSIDE TEMPERATURE MwT-PH11'F Chip and 71 Pkg



Absolute Maximum Rating

Symbol	Parameter	Units	Cont Max1	Absolute Max2	
VDS	Drain to Source Volt.	V	8.0	8.5	
Tch	Channel Temperature	°C	+150	+175	
Tst	Storage Temperature	°C	-65 to +150	+175	
Pin	RF Input Power	mW	500	700	

Notes

- 1. Exceeding any one of these limits in continuous operation may reduce the mean-time- to-failure below the design goal.
- 2. Exceeding any one of these limits may cause permanent damage.



MWT-PH11F 12 GHz Medium Power AlGaAs/InGaAs PHEMT

S-Parameters

Freq.	PARAMETER Vds=8V, Ids Freq. S11		\$21		S12		\$22		K	GMAX
GHz	dB	Ang (°)	dB	Ang (°)	dB	Ang (°)	dB	Ang (°)		dB
1	-0.763	-138.853	22.454	104.657	-32.283	24.086	-10.055	-141.617	0.198	27.369
2	-0.782	-162.491	16.940	88.129	-31.888	16.281	-9.522	-155.023	0.355	24.414
3	-0.782	-172.386	13.489	78.106	-31.691	14.649	-9.063	-158.581	0.506	22.590
4	-0.765	-178.660	10.927	69.522	-31.687	15.944	-8.493	-159.528	0.649	21.307
5	-0.779	176.661	9.000	63.408	-31.705	16.914	-8.075	-160.496	0.825	20.353
6	-0.724	172.409	7.458	56.174	-31.325	20.538	-7.713	-161.777	0.874	19.392
7	-0.626	167.937	6.081	48.726	-31.182	22.865	-7.138	-164.037	0.847	18.632
8	-0.649	165.699	4.698	42.454	-31.102	25.258	-6.507	-164.374	0.975	17.900
9	-0.682	162.970	3.306	35.880	-30.956	28.978	-6.067	-167.164	1.176	14.588
10	-0.706	159.631	2.315	30.167	-30.553	31.225	-5.566	-168.217	1.251	13.41
11	-0.617	156.253	1.426	23.731	-30.040	33.719	-5.113	-171.388	1.098	13.822
12	-0.573	154.102	0.426	18.200	-29.763	36.298	-4.710	-173.542	1.065	13.53
13	-0.658	151.563	-0.521	12.655	-29.071	38.997	-4.329	-176.096	1.221	11.44
14	-0.643	149.995	-1.500	8.204	-28.735	41.598	-3.953	-178.800	1.235	10.69
15	-0.597	146.658	-2.154	3.022	-27.751	42.833	-3.642	178.934	1.053	11.39
16	-0.521	144.626	-3.083	-2.331	-27.212	44.068	-3.379	175.452	0.923	12.06
17	-0.550	142.697	-3.861	-6.552	-26.676	42.849	-3.077	172.963	0.924	11.408
18	-0.545	141.770	-4.635	-10.760	-25.991	43.314	-2.809	169.448	0.866	10.678
19	-0.556	140.063	-5.485	-14.316	-25.389	43.335	-2.692	167.217	0.886	9.952
20	-0.412	136.960	-6.123	-19.011	-24.765	43.199	-2.487	165.318	0.605	9.321
21	-0.618	135.138	-7.123	-22.456	-24.364	44.264	-2.292	162.103	0.966	8.620
22	-0.571	133.208	-7.758	-25.816	-23.792	42.959	-2.022	159.794	0.804	8.017
23	-0.489	132.372	-8.341	-28.947	-23.064	40.411	-2.015	156.412	0.652	7.362
24	-0.571	130.488	-9.202	-31.466	-23.041	39.155	-1.989	154.674	0.842	6.919
25	-0.589	128.512	-9.857	-35.045	-22.395	37.303	-1.675	150.780	0.750	6.269
26	-0.495	127.182	-10.469	-37.584	-22.030	36.296	-1.572	148.225	0.610	5.781

ORDERING INFORMATION:

When placing order or inquiring, please specify wafer number, if known. For details of Safe Handling Procedure please see supplementary information in available PDF on our website www.mwtinc.com. For package information, please see supplementary application note in PDF format by clicking located on our website.

Chip MwT-PH11F Package 71 MwT-PH1171F