Winding Type Chip Inductor

SWF1608RIV-SERIES

1. Features

- 1. Ferrite core wire wound construction.
- 2. High Reliability due to wire wound type construction.
- 3. Small footprint as well as low profile.
- 4. 100% Lead (Pb) & Halogen-Free and RoHS compliant.
- 5. Operating temperature-55~+125°C (Including self temperature rise).
- 6. These products provide low DC resistance and high current.
- 7. Precision inductance tolerance is available.
- 8. High reliability -Reliability comply with AEC-Q200.
- 9. Application for DC power line.

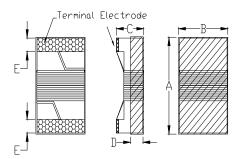
Digital camera and other electronic equipment

Personal computers, Hard disk drives

Mobile Device / Handheld Device / LowProfile Device / Panel

xDSL modem and Cable modem

2. Dimensions



Size	Α	В	С	D	E
SWF1608	1.60±0.20	1.00±0.20	1.00±0.10	0.60 ref.	0.35±0.10

Unit:mm

3. Part Numbering



A: Series

B: Dimension L x W

C: Control S/N

 D: Material
 I3C

 E: Category Code
 V=Vehicle

 F: Inductance
 2R2=2.20uH

 G: Inductance Tolerance
 K=±10%, M=±20%

4. Specification

TAI-TECH Part Number	Inductance (uH)	Tolerance	Test Frequency (Hz)	Q/MHz Typ.	SRF (MHz) Typ.	DCR (Ω) ±30%	Isat (mA) Typ.	Irms (mA) Typ.
SWF1608RIV-R10	0.10	K,M	0.5V/7.9M	13/7.9	1150	0.063	1700	1400
SWF1608RIV-R15	0.15	K,M	0.5V/7.9M	13/7.9	1050	0.074	1700	1300
SWF1608RIV-R27	0.27	K,M	0.5V/7.9M	13/7.9	1000	0.12	1400	1100
SWF1608RIV-R33	0.33	K,M	0.5V/7.9M	13/7.9	1100	0.13	1300	1000
SWF1608RIV-R47	0.47	K,M	0.5V/7.9M	13/7.9	900	0.18	1100	900
SWF1608RIV-R56	0.56	K,M	0.5V/7.9M	13/7.9	630	0.20	1100	800

TAI-TECH Part Number	Inductance (uH)	Tolerance	Test Frequency (Hz)	Q/MHz Typ.	SRF (MHz) Typ.	DCR (Ω) ±30%	Isat (mA) Typ.	Irms (mA) Typ.
SWF1608RIV-R68	0.68	K,M	0.5V/7.9M	13/7.9	510	0.22	900	750
SWF1608RIV-1R0	1.0	K,M	0.5V/7.9M	16/7.9	390	0.32	860	700
SWF1608RIV-1R5	1.5	K,M	0.5V/7.9M	16/7.9	160	0.40	720	600
SWF1608RIV-2R2	2.2	K,M	0.5V/7.9M	16/7.9	103	0.56	600	580
SWF1608RIV-3R3	3.3	K,M	0.5V/7.9M	16/7.9	66	0.70	500	500
SWF1608RIV-4R7	4.7	K,M	0.5V/7.9M	16/7.9	51	0.97	400	420
SWF1608RIV-5R6	5.6	K,M	0.5V/7.9M	16/7.9	47	1.10	380	380
SWF1608RIV-6R8	6.8	K,M	0.5V/7.9M	16/7.9	43	1.50	340	340
SWF1608RIV-8R2	8.2	K,M	0.5V/7.9M	16/7.9	40	1.68	300	300
SWF1608RIV-100	10	K,M	0.5V/2.5M	14/2.5	36	1.85	280	280
SWF1608RIV-150	15	K,M	0.5V/2.5M	14/2.5	29	2.60	240	240
SWF1608RIV-180	18	K,M	0.5V/2.5M	14/2.5	28	2.90	220	220
SWF1608RIV-220	22	K,M	0.5V/2.5M	14/2.5	24	3.61	200	200

Note:

Measurement board data

Material: FR4

Board dimensions : 100 X 50 X 1.6t mm

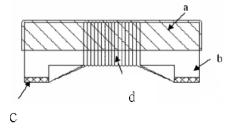
Pattern dimensions: 45 X 30 mm (Double side board)

Pattern thickness $\,$: 50 $\,\mu$ m

• All test data referenced to 25°C ambient

5. Materials

No.	Description	Specification
a.	Upper Plate	UV Glue
b.	Core	Ferrite Core
С	Termination	Ag/Ni/Sn
d	Wire	Enameled Copper Wire



Note:

Conformal coating:

- a. The inductance value may change due to high cure-stress of resin to be used for coating/molding products.
- An open circuit issue may occur by mechanical stress caused by the resin, amount/cured shape of resin, or operating condition etc. Please pay careful attention when selecting resin in case of coating/molding process.
 Prior to use the coating resin, please make sure no reliability issue is observed.

6. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-55~+125℃ (Including self - temperature rise)	
Storage temperature	-55~+125°ℂ (on board)	
Electrical Performance Tes	t	
Inductance		Keysight E4991B, Keysight 4980AL
Q		Agilent-4287, Agilent-4285
SRF	Refer to standard electrical characteristic list	Keysight E4991B
DCR		Agilent-34420A
ISAT		Agilent-4338B Applied the current to coils, the inductance change shall be less
ISAI		than 20% to initial value.
IRMS	∆T≤40°C	Heat Rated Current (Irms) will cause the coil temperature rise \triangle T(${}^{\circ}$ C) without core loss. 1.Applied the allowed DC current. 2.Temperature measured by digital surface thermometer

iability Test			
		Preconditioning: Run through reflow for (IPC/JEDECJ-STD-020F Classification Unpowered Temperature : 125±2°C	
High Temperature Exposure(Storage) AEC-Q200		Upper Temperature: maximum specified maximum specified storage temperature	
AEC-Q200		Minimum test temperature shall be 85°C (For ferrite EMI suppressors/filters only)	
		Duration: 1000hrs Min.	
	4	Measured at room temperature after pla Preconditioning: Run through reflow for	
		(IPC/JEDECJ-STD-020F Classification Unpowered	
		Lower Temperature of the Chamber: -40°C (For Inductors/Transformers)	
		-55°C (For ferrite EMI suppressors/filters))
		Upper Temperature of the Chamber: ma temperature and shall not exceed 125℃	ximum specified operating
Temperature Cycling		Condition for 1 cycle	
AEC-Q200		Step1 : -40±2°C 30min Min. Step2 : 125±2°C transition time 1min M.	AX.
	Appearance : No damage.	Step3 : 125±2°C 30min Min. Step4 : Dwell Time (Soak Time)	
	Inductance: within±10% of initial value Q: Shall not exceed the specification value.	15 minutes minimum,	
	DCR: within±15% of initial value and shall not	30 minutes minimum if componer Transition Time: 1 minute maximum	it weighs above 28g
	exceed the specification value	Number of cycles: 1000 Measured at room temperature at least 2	24 hours after test conclus
	1	Preconditioning: Run through reflow for	3 times.
		(IPC/JEDEC J-STD-020F Classification Unpowered(For Inductors/Transformers)	
Humidity Bias		Apply 10% of maximum rated power. (For ferrite EMI suppressors/filters)	
(AEC-Q200)		Humidity: 85±3% R.H,	
		Temperature : 85°ℂ±2°ℂ	
		Duration: 1000hrs Min. Measured at room temperature after pla	cing for 24±4hrs.
	7	Preconditioning: Run through reflow for (IPC/JEDECJ-STD-020F Classification	
		Temperature : 125±2°C	renow i ronics)
High Temperature Operating Life (AEC-Q200)		Upper Temperature of the Chamber: mattemperature (not including heat rise) at mot exceed 125°C. (For Inductors/Trans) Temperature of the Chamber: maximum temperature up to 150°C (For ferrite EM Duration: 1000hrs Min. with 100% rated Measured at room temperature after pla Rated IL applied. (For ferrite EMI suppression of the composition of the compositio	naximum rated power and ormers) specified operating III suppressors/filters) I current. cing for 24±4 hrs.
External Visual	Appearance : No damage.	Inspect device construction, marking and Electrical Test not required.	d workmanship. Pre and Pe
Physical Dimension	According to the product specification size measurement	Verify physical dimensions to the specification. Pre and Post Electrical Terminal Programmers of the Post Electrical Programmers of the Post E	
		Test THT component lead integrity only. Test Condition A (pull test)	
		Nominal cross- sectional	Force (N)
		area (mm²)	Force (N)
		≤ 0.05 0.06 to 0.10	<u>1</u> 2.5
		0.11 to 0.20	<u>5</u>
		0.21 to 0.50	<u>10</u>
		0.51 to 1.20 > 1.20	<u>20</u> 40
	Appearance: No damage.	Test Condition C (wire-lead bend test):	40
Terminal Strength (for axial and radial THT	Inductance: within±10% of initial value Q: Shall not exceed the specification value.	Section Modulus (Zx) (mm ³)	Force (N)
components)	DCR: within±15% of initial value and shall not	≤ 1.5x10 ⁻³	0.5
	exceed the specification value	1.6x10 ⁻³ to 4.2x10 ⁻³	<u>1.25</u>
		4.3x10 ⁻³ to 1.2x10 ⁻²	<u>2.5</u>
		1.3x10 ⁻² to 0.5x10 ⁻¹ 0.6x10 ⁻¹ to 1.9x10 ⁻¹	<u>5</u> 10
		> 1.9x10 ⁻¹	<u>20</u>
		For round terminations: ZX = (πd3)/32 w For strip terminations: ZX = (ba2)/6 whe rectangular strip perpendicular to the be	re is the thickness of the

Item	Performance			Test Co	nditio	n		
Resistance to Solvents		recommen	ded paramet	chemical and for ers (i.e. solution d components	n tempera	ature and	d immersio	on time).
		Туре	Peak value (g's)	Normal duration (D) (Vave form		ocity (Vi)ft/sec
Mechanical Shock		SMD	100	6		alf-sine		2.3
Wedianical Shock		THT	100	6	На	alf-sine	12	2.3
Vibration		(18 shock Precondition (IPC/JEDE Oscillation Equipment Total Ampl	s). EC J-STD-02 Frequency: : Vibration citude: 5g ne: 12 hours	ection along rough reflow fo OF Classification 10Hz~2kHz~ hecker	or 3 times on Reflow 10Hz for	Profiles 20 minu	tes	ons)
			itions B or C					
		Solder technique simulation		Temperature (°C)	Time(s)	ramp/ir and e	perature mmersion mersion rate	Number of heat cycles
	Appearance : No damage.	Dip	В	260 ±5 (solder temp)	10±1		mm/s 6mm/s	1
	Inductance: within±10% of initial value Q: Shall not exceed the specification value. DCR: within±15% of initial value and shall not exceed the specification value	Wave: Topside board-mo nt produc	u C	260 ±5 (solder temp)	20±1			1
Resistance to Soldering Heat		Continenta Tempera Tempera Component Component Size Size Component Size Size	T servin T servin Ramp up to 150°C to to to 150°C to	Time to Those 3190° 2110s 2200°C C 2110s 2200°C r lead-free soldering the centre top of the c	w process	Tpeak tp tl ramp dow	Time	mp down 16 1°C/s e e e e e e e e e e e e e e e e e e e
ESD		Discharge Test method Test mode	tact and Air I Waveform to dd: AEC-Q20 : Contact Di level : 4 KV	ischarge	SIVE CO	MPONE	NT HBM E	ESD

Item	Performance		Test	Condition			
		Through-hole Technology (THT: Method A1, Coating Durability Category 2, SMD: Method B1, Coating Durability Category 2 Method D, Coating Durability Category 2 Magnification 50x Pre and Post Electrical Test not required. Non-soldered type mounting/attach are not applicable.					
		参照	Method A1	Method B1	Method D		
	Appearance : No damage.	焊接工藝	再流焊	其他器件的再流焊	無鉛焊接		
Solderability	Inductance: within±10% of initial value Q: Shall not exceed the specification value.		, 400-20				
,	DCR: within±15% of initial value and shall not exceed the specification value	焊接類型	錫銀銅焊料	錫銀銅焊料	錫銀銅焊料		
		浸入助焊劑時	5-10s	5-10s	5-10s		
		浸入錫爐角度	20 ° ~45 °	20 ° ~45 °	20 ° ~45 °		
		焊料温度	245±5°C	245±5°C	260±5°C		
		浸入焊料時間	5+0/-0.5s	5+0/-0.5s	30+5/-0s		
		浸入和提出速	25±6mm/s	25±6mm/s	25±6mm/s		
Electrical Characterization	Refer Specification for Approval	Parametrically test Min, Max, Mean a operating tempera Pre and Post Elect	ind Standard de tures.	pple size requirements, viation at room as we uired	summary to show Il as Min and Max		
Flammability	In accordance with Referenced Standards	UL-94 or IEC 6069	5-11-5				
Board Flex (SMD)	Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. DCR: within±15% of initial value and shall not exceed the specification value	Place the 100mm below Figure with t mechanical means	D-020F Classific X 40mm board he component fate to apply a force ation of the applity once to the bost Solder Chip	ation Reflow Profiles) into a fixture similar to acing down. The appara which will bend the be ed forces shall be 60 (atus shall consist of oard (D) x = 2 mm + 5) sec. The force testing		
Terminal Strength(SMD)			radius 0	press tool	wide thickness shear force		

Note:

The main function of UV coating is for the pick and place of SMT machine.

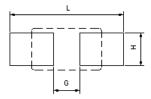
For UV coating on the top side. In rare cases, some very small cracks may appear on the coating surface after being exposed after many times of IR reflow, long times of high and low temperature, high humidity and temperature cycles.

Small cracks on UV coating will not affect functions of this product.

7. Soldering and Mounting

7-1. Recommended PC Board Pattern

Chip size						Pattern			
Series	Туре	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	L(mm)	G(mm)	H(mm)
SWF	1608	1.60±0.2	1.0±0.2	1.0±0.1	0.60 ref	0.35±0.1	1.92	0.92	1.02



7-2. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

7-2.1 Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020F)

7-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Figure 2.)

- Never contact the ceramic with the iron tip
- · Use a 20 watt soldering iron with tip diameter of 1.0mm

- 350°C tip temperature (max)
- 1.0mm tip diameter (max)
- · Limit soldering time to 4~5sec.

Fig.1 Soldering Reflow

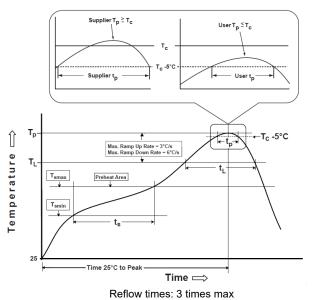
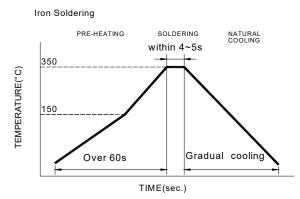


Fig.2 Iron soldering temperature profiles



Iron Soldering times: 1 times max

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat -Temperature Min(T _{smin}) -Temperature Max(T _{smax}) -Time(t _s)from(T _{smin} to T _{smax})	150°C 200°C 60-120seconds
Ramp-up rate(T _L to T _p)	3°ℂ/second max.
$\label{eq:Liquidus} \begin{array}{c} \text{Liquidus temperature}(T_L) \\ \text{Time}(t_L) \\ \text{maintained above } T_L \end{array}$	217°C 60-150 seconds
Classification temperature(T _c)	See Table (1.2)
$\label{eq:total_final_continuous} \mbox{Time}(t_p) \mbox{ at Tc-} 5^{\circ}\mbox{\mathbb{C}} \mbox{ (Tp should be equal to or less than Tc.)}$	< 30 seconds
Ramp-down rate(T _p to T _L)	6°C/second max.
Time 25°C to peak temperature	8 minutes max.

Tp: maximum peak package body temperature, **Tc**: the classification temperature.

For user (customer) **Tp** should be equal to or less than **Tc**.

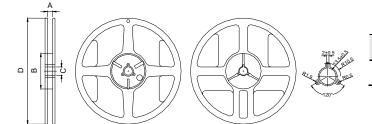
Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

	Package	Volume mm ³	Volume mm ³	Volume mm ³
	Thickness	<350	350-2000	>2000
	<1.6mm	260°C	260°C	260°C
PB-Free Assembly	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020F

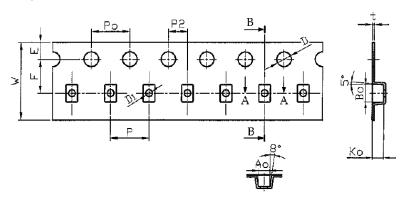
8. Packaging Information

8-1. Reel Dimension



Type	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	9.0±0.5	60.0±2.0	13.5±0.5	178.0±2.0

8-2. Tape Dimension / 8mm(black anti-static electricity carrier tape)



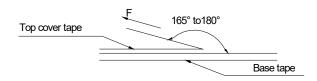
7"x12mm

Series	W(mm)	P(mm)	E(mm)	F(mm)	P2(mm)	D(mm)	D1(mm)	Bo(mm)	Ao(mm)	Ko(mm)	Po(mm)	t(mm)
SWF1608	8.00±0.10	4.00±0.10	1.75±0.10	3.50±0.05	2.00±0.05	1.50+0.100.00	0.70±0.10	1.78±0.10	1.21±0.10	1.21±0.10	4.00±0.10	0.20±0.05

8-3. Packaging Quantity

SWF	1608		
Chip / Reel	4000		
Reel Size	7"x8mm		

8-4. Tearing Off Force



The force for tearing off cover tape is 15 to 80 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed		
(℃)	(%)	(hPa)	mm/min		
5~35	45~85	860~1060	300		

Application Notice

• Storage Conditions(component level)

To maintain the solderability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020F standard-MSL, level 1.
- $3. \ \mbox{Recommended}$ products should be used within 12 months from the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
 - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
 - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.