

Customer Advisory

ADV2413

Minor Change to the Barcode Labels of Products in the Agilex™ FPGA Portfolio and Stratix® 10 FPGAs & SoC FPGAs

Issue Date: 2024-10-04

Description



Altera is notifying customers on a minor barcode label content change affecting all devices in the Agilex™ FPGA Portfolio and Stratix® 10 FPGAs and SoC FPGAs

Additional content will be shown on the lower right of the barcode labels applied to the Moisture Barrier Bag (MBB) and the intermediate box (iBOX).

This new content is for a second "MM#", designated as (31P), to be used solely for Altera internal product identification. All other label fields remain the same.

See Table 1 for the details and comparison to the existing label.

Table 1

Change From	Change To
 <p>Existing barcode label applied to the Moisture Barrier Bag (MBB) and Intermediate Box (iBOX)</p>	 <p>New barcode label to the MBB and iBOX</p> <ul style="list-style-type: none"> - New content (highlighted in yellow) is a second MM# with a barcode (31P).

Recommended Actions

Customers are requested to review and take note of the change.

This is only a label change and there is no change to the actual products.

There is no impact to form, fit, function, quality and reliability.

Products Affected

All devices in the Agilex FPGA Portfolio and Stratix 10 FPGAs and SoC FPGAs.

The list of affected part numbers (OPNs) can be downloaded in Excel form from this link:
<https://cdrdv2.intel.com/v1/dl/getContent/827335>

Change Implementation

Milestone	Date
Earliest shipment using the modified barcode label	December 1, 2024
Note: From this date, you may expect to see shipments using either or both previous and the modified barcode labels, until products using the previous labels are depleted.	

Reason for Change

The addition of a second MM# is solely for Altera internal product identification and transaction purposes.

Contact

For more information, please contact your local Sales representative, or submit a question or request at the My Intel support page, log in via:
<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Customer Notifications Subscription

If you want to receive customer notifications by email, please follow the instructions in [ADV 2209](#).

Altera references J-STD-046 guidelines for PCN.

In accordance with J-STD-046, this change is deemed acceptable to the customer if no acknowledgment is received within 30 days from the date of notification.

Document Revision History

Date	Version	Description
2024-10-04	1	Initial release

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