

μClamp2011PW Ultra Small μClamp® 1-Line, 20V ESD Protection

Description

 μ Clamp[®] series of TVS arrays are designed to protect sensitive electronics from damage or latch-up due to ESD and surge. They feature large cross-sectional area junctions for conducting high transient currents. They offer desirable characteristics for board level protection including fast response time, low operating and clamping voltage, and no device degradation.

 μ Clamp2011PW is in a DFN 1.0 x 0.6 x 0.55mm 2-Lead package. Each device will protect one line operating at 20 volts. It gives the designer the flexibility to protect single lines in applications where arrays are not practical. The combination of small size and high ESD surge capability makes them ideal for use in portable applications such as cellular phones, tablets, and notebook computers.

Features

- High ESD withstand voltage: ±27kV (air) and ±22kV (contact) per IEC 61000-4-2
- High lightning surge capability: 3A (tp=8/20μs) per IEC 61000-4-5
- Protects one line
- Low ESD clamping voltage
- Working voltage: ±20V
- Low maximum leakage current: 100nA
- Solid-state silicon-avalanche technology

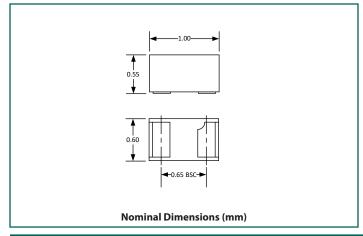
Mechanical Characteristics

- Package: DFN 1.0 x 0.6 x 0.55mm 2-Lead
- Halogen Free, RoHS/WEEE compliant
- · Lead Finish: Pb-Free
- · Marking: Marking code
- · Packaging: Tape and Reel

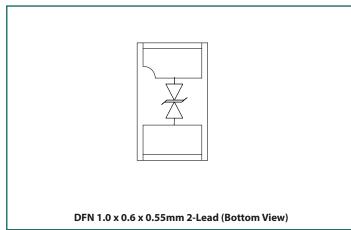
Applications

- Cellular Handsets & Accessories
- Notebook Computers
- Tablets
- · Portable Instrumentation
- Peripherals

Package Dimension



Schematic & Pin Configuration



Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp = $8/20\mu s$)	P _{PK}	110	W
Peak Pulse Current (tp = 8/20μs)	I _{PP}	3	A
ESD per IEC 61000-4-2 (Air) ⁽¹⁾ ESD per IEC 61000-4-2 (Contact) ⁽¹⁾	V _{ESD}	±27 ±22	kV
Operating Temperature	T _{OP}	-40 to +85	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Reverse Stand-Off Voltage	V _{RWM}				20	V
Reverse Breakdown Voltage	V _{BR}	I _t = 1 mA	22	24	28	V
Reverse Leakage Current	I _R	V _{RWM} = 20V			100	nA
Clamping Voltage	V _c	I _{pp} = 1A, tp = 8/20μs		29	31	V
		$I_{pp} = 3A$, $tp = 8/20 \mu s$		35	37	
ESD Clamping Voltage ⁽²⁾	V _c	I _{TLP} = 4A, tp = 0.2/100ns		29		V
		ι _{π.Ρ} =16A, tp = 0.2/100ns		35		
Dynamic Resistance ^{(2),(3)}	R _{DYN}	tp = 0.2/100ns		0.5		Ω
Junction Capacitance	C _J	$V_R = 0V, f = 1MHz$		10	13	рF

Notes:

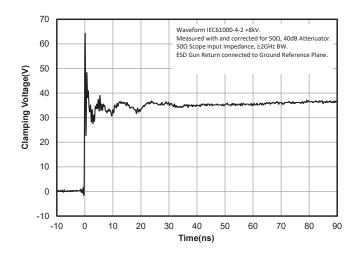
¹⁾ ESD gun return path connected to ESD ground plane.

²⁾ Transmission Line Pulse Test (TLP) Settings: tp = 100 ns, tr = 0.2 ns, I_{TLP} and V_{TLP} averaging window: t1 = 70 ns to t2 = 90 ns.

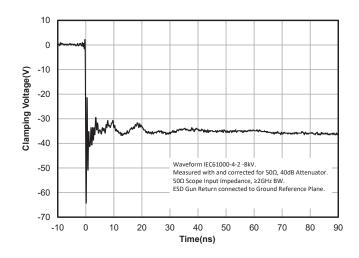
³⁾ Dynamic resistance calculated from $I_{TLP} = 4A$ to $I_{TLP} = 16A$

Typical Characteristics

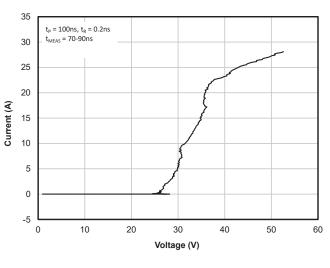
ESD Clamping (+8kV Contact per IEC 61000-4-2)



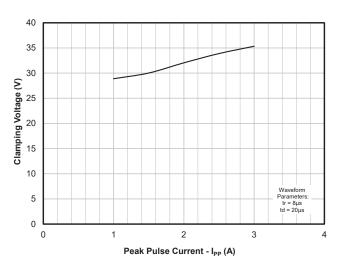
ESD Clamping (-8kV Contact per IEC 61000-4-2)



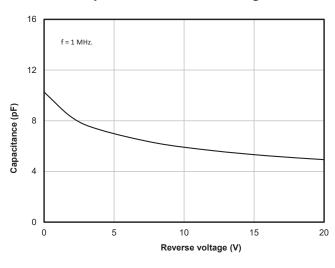
TLP Characteristic (Positive Pulse)



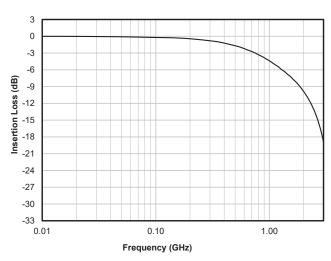
Clamping Voltage (tp=8/20µs)



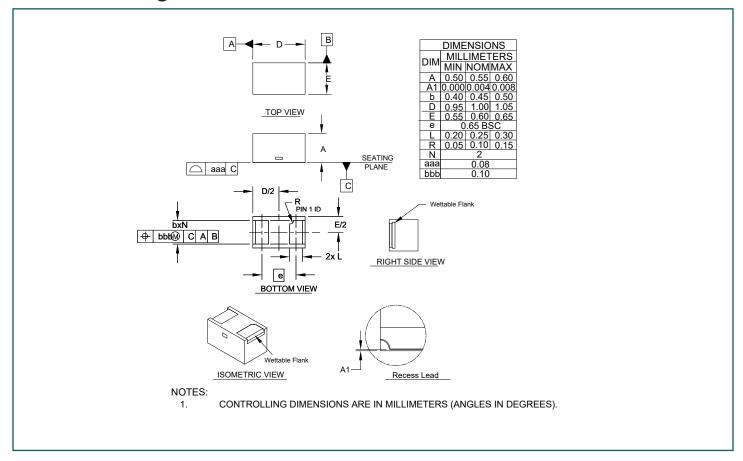
Capacitance vs. Reverse Voltage



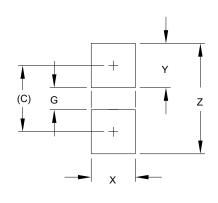
Insertion Loss-S21



Outline Drawing - DFN 1.0 x 0.6 x 0.55mm 2-Lead



Land Pattern - DFN 1.0 x 0.6 x 0.55mm 2-Lead

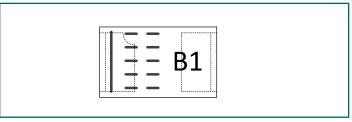


DIMENSIONS			
DIM	MILLIMETERS		
С	(0.90)		
G	0.30		
Χ	0.60		
Υ	0.60		
Z	1.50		

NOTES:

- 1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
- THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY.
 CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR
 COMPANY'S MANUFACTURING GUIDELINES ARE MET.

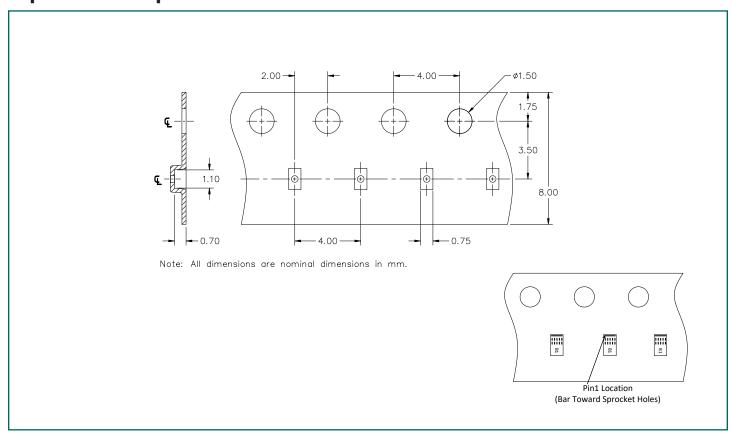
Marking Code



Notes:

- 1. Device is electrically symmetrical
- 2. Marking will also include line matrix date code
- 3. Bar indicates Pin 1 location

Tape and Reel Specification



Ordering Information

Part Number	Qty per Reel	Reel Size		
μClamp2011PW.C	3,000	7"		
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