



Customer Information Notification

202405015I : i.MX 8ULP Errata Rev 4.1

Note: This notice is NXP Company Proprietary.

Issue Date: Jun 01, 2024

Effective Date: Jun 02, 2024

Management summary

NXP Semiconductors announces errata (iMX8ULPA2_P40A) revision from Rev.3 to Rev.4.1 for the i.MX 8ULP family of devices.

[X] Errata

Notification Overview

Description

NXP Semiconductors announces errata (iMX8ULPA2_P40A) revision from Rev.3 to Rev.4.1 for the i.MX 8ULP family of devices. The revision history included in the updated document provides a detailed description of the changes.

Changes are summarized below.

Added ERR052202: Prolonged assertion of external reset can cause the part to not come out of reset.

Description: There is an interaction between external reset (reset0_b) and the internally generated reset request which may prevent the i.MX 8ULP processor from booting correctly. In the failing use case, the internally generated reset request occurs near the time the external reset (reset0_b) is de-asserted leading to boot failure.

Workaround: To prevent this issue the external reset (reset0_b) should be released immediately after the Power on Reset (POR) sequence outlined in the product data sheet. Refer to the standard EVK (MCIMX8ULP-EVK) configuration and supported BSP as an example.

The i.MX 8ULP EVK (MCIMX8ULP-EVK) does not present this issue. Users can refer to the board design files posted on the product EVK page below as reference for their board designs.

<https://www.nxp.com/design/design-center/development-boards-and-designs/i-mx-evaluation-and-development-boards/i-mx-8ulp-evaluation-kit:MCIMX8ULP-EVK>

The i.MX 8ULP family errata is attached to this notification and can also be found at:

<https://www.nxp.com/products/processors-and-microcontrollers/arm-processors/i-mx-applications-processors/i-mx-8-applications-processors/i-mx-8ulp-applications-processor-family:i.MX8ULP#documentation>

Reason

The errata has been updated with to include a new erratum: ERR052202.

12NC	Orderable Part Number
935444326557	MIMX8UD3CVP08SC
935449631557	MIMX8UD3DVK08SC
935447978557	MIMX8UD3DVP08SC
935444328557	MIMX8UD5CVP08SC
935449632557	MIMX8UD5DVK08SC
935449499557	MIMX8UD5DVP08SC
935447979557	MIMX8UD7CVP08SC
935449633557	MIMX8UD7DVK08SC
935447981557	MIMX8UD7DVP08SC
935444333557	MIMX8US3CVP08SC
935447983557	MIMX8US3DVK08SC
935449501557	MIMX8US3DVP08SC
935444335557	MIMX8US5CVP08SC
935447984557	MIMX8US5DVK08SC
935447982557	MIMX8US5DVP08SC

Identification of Affected Products

Product identification does not change

Anticipated Impact on Form, Fit, Function, Reliability or Quality

See "ERR052202: Prolonged assertion of external reset can cause the part to not come out of reset." for details.

Data Sheet Revision

No impact to existing data sheet

Additional information

Additional documents: [view online](#)

Contact and Support

For all inquiries regarding the ePCN tool application or access issues, please contact NXP "Global Quality Support Team".

For all Quality Notification content inquiries, please contact your local NXP Sales Support team.

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NXP Quality Management Team.

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