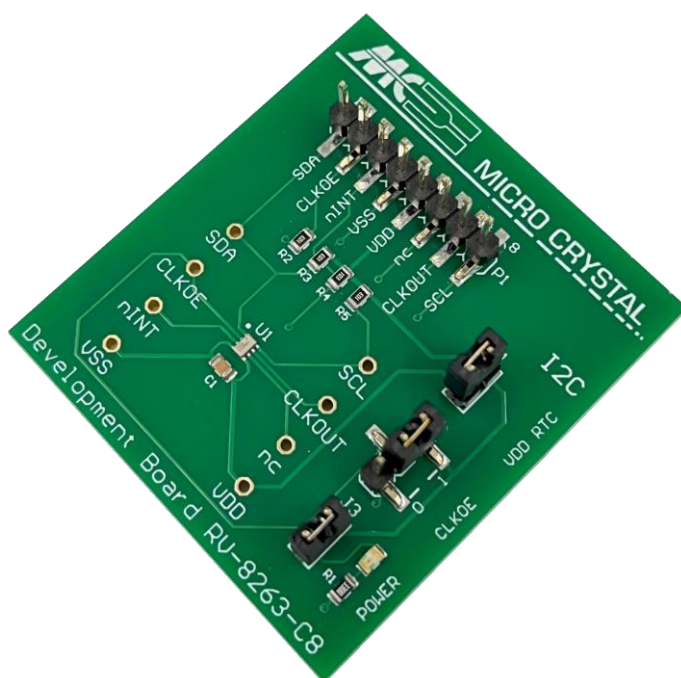


DEVELOPMENT BOARD



RV-8263-C8

Low-Power Real-Time Clock / Calendar Module

RV-8263-C8

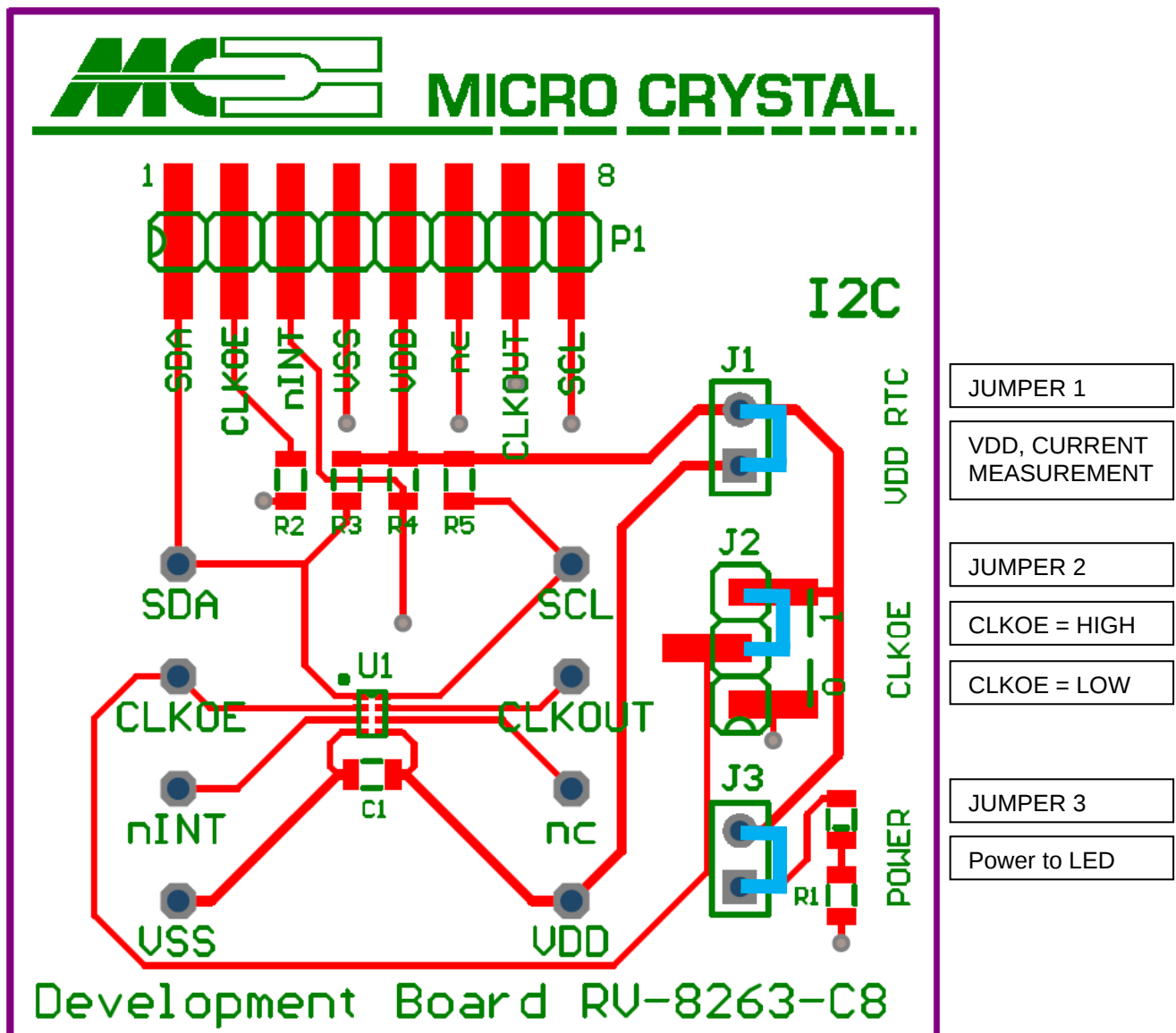
The RV-8263-C8 is soldered (U1) onto the Development Board.

Every pin is either accessible at test pins 1 – 8 or at the test vias situated around the device.

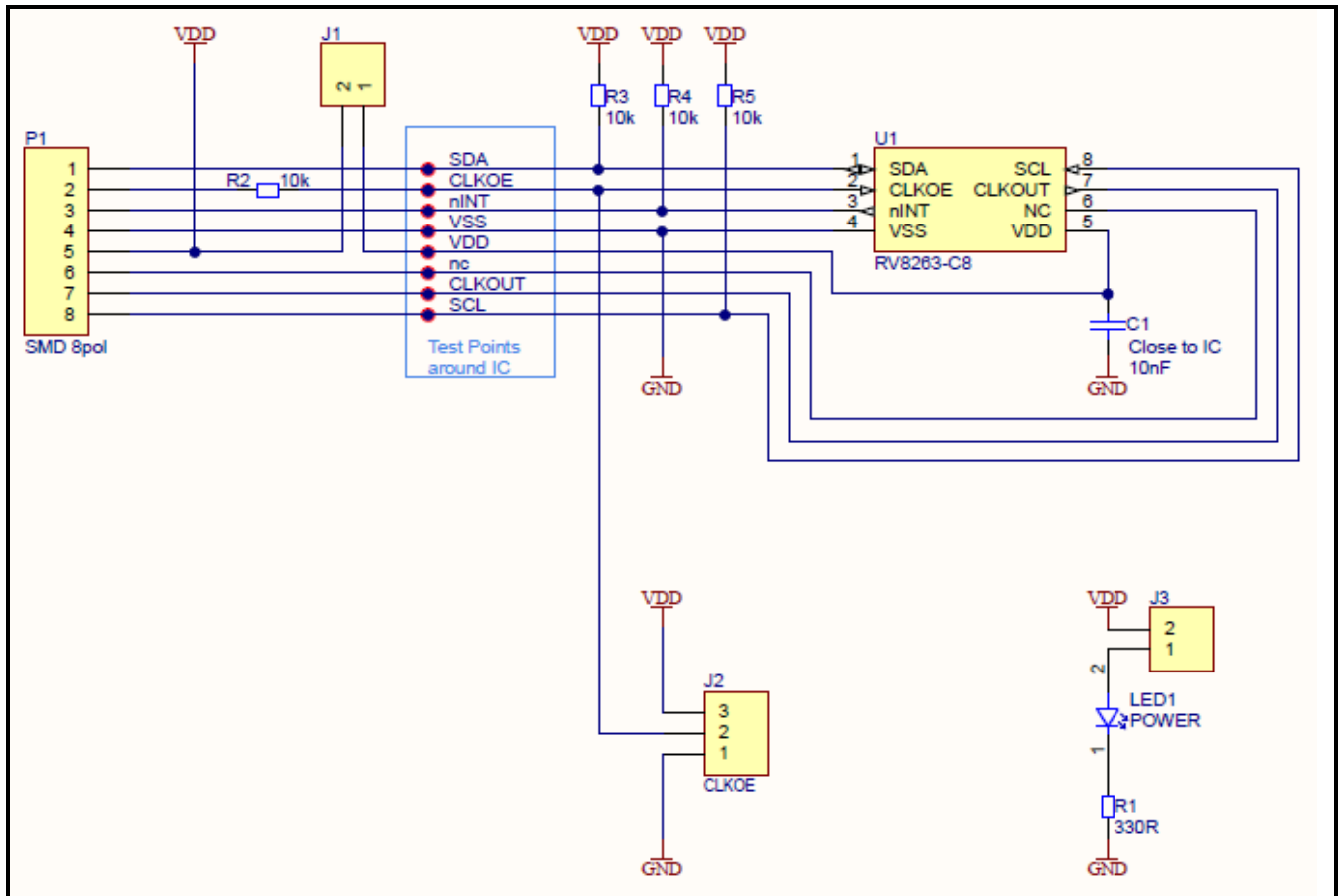
The following passive components are already soldered on the board:

C1	10 nF	Decoupling capacitor between V_{SS} and V_{DD} .
R1	330 Ω	Current limiting resistor for LED.
LED	green	Supply on, current consumption of the LED must be considered.
R2	10 k Ω	Protection resistor to prevent short-circuit between external CLKOE signal and Jumper.
R3	10 k Ω	Pull-up resistor SDA to V_{DD} .
R4	10 k Ω	Pull-up resistor $\overline{INT}$ to V_{DD} .
R5	10 k Ω	Pull-up resistor SCL to V_{DD} .

DEVELOPMENT BOARD



SCHEMATICS



PINOUT RV-8263-C8

		<table border="1"> <tr> <td># 1</td><td>SDA</td><td># 8</td><td>SCL</td></tr> <tr> <td># 2</td><td>CLKOE</td><td># 7</td><td>CLKOUT</td></tr> <tr> <td># 3</td><td>$\overline{\text{INT}}$</td><td># 6</td><td>NC</td></tr> <tr> <td># 4</td><td>Vss</td><td># 5</td><td>VDD</td></tr> </table>		# 1	SDA	# 8	SCL	# 2	CLKOE	# 7	CLKOUT	# 3	$\overline{\text{INT}}$	# 6	NC	# 4	Vss	# 5	VDD
# 1	SDA	# 8	SCL																
# 2	CLKOE	# 7	CLKOUT																
# 3	$\overline{\text{INT}}$	# 6	NC																
# 4	Vss	# 5	VDD																

PIN DESCRIPTION

Symbol	Pin #	Description
SDA	1	I ² C Serial Data Input-Output; open-drain; requires pull-up resistor.
CLKOE	2	Input to enable the CLKOUT pin. If CLKOE is HIGH, the CLKOUT pin is in output mode. When CLKOE is tied to Ground, the CLKOUT pin is LOW.
$\overline{\text{INT}}$	3	Interrupt Output; open-drain; active LOW; requires pull-up resistor; Used to output alarm, minute, half minute, countdown timer and compensation Interrupt signals.
VSS	4	Ground.
VDD	5	Power Supply Voltage
NC	6	Not connected. Is internally connected and should be left floating.
CLKOUT	7	Clock Output; push-pull; controlled by CLKOE. If CLKOE is HIGH (VDD), the CLKOUT pin drives the square wave of 32.768 kHz, 16.384 kHz, 8.192 kHz, 4.096 kHz, 2.048 kHz, 1.024 kHz or 1 Hz (Default value is 32.768 kHz). When CLKOE is tied to Ground, the CLKOUT pin is LOW.
SCL	8	I ² C Serial Clock Input; requires pull-up resistor.

Datasheet and Application-Manual are available for download under: <https://www.microcrystal.com>