PCN	Numb	er:	2024	032	7004.2		PCI	N D	ate:	March 28, 2024	
Title: Qualification of RFAB using qualified Process Technology, Die Change & BOM options											
Cus	tomer	Contact:		Cha tear	nge Managemen n	t	Dej	pt:		Quality Services	
Pro	posed 1	L st Ship Date	-	Sept 202	tember 24, 4	Estir	stimated Sample Availability:			April 27, 2024*	
*Sa	mple re	equests rece	ived a	afte	r April 27, 202	4 will n	ot b	e s	upporte	ed.	
Cha	nge Ty	pe:									
	Assem	bly Site			Design				Wafer Bump Material		
\boxtimes	Assem	bly Process			Data Sheet				Wafer I	Bump Process	
\boxtimes	Assem	bly Materials			Part number ch	nange		X	Wafer Fab Site		
	Mechar	nical Specifica	tion		Test Site			X	Wafer Fab Materials		
	Packing/Shipping/ Labeling				Test Process			X	Wafer I	Fab Process	
	Labelin	ıg									

PCN Details

Description of Change:

Texas Instruments is pleased to announce the addition of RFAB using the LBC9 qualified process technology and additional & BOM options for the device listed below.

С	urrent Fab Si	te	Additional Fab Site			
Current Fab Site	Process	Wafer Diameter	Additional Fab Site	Process	Wafer Diameter	
SH-BIP1	HCMOS	150 mm	RFAB	LBC9	300 mm	

The die was also changed as a result of the process change.

Group 1 BOM Table (RFAB/Process migration, die change only):

There is no BOM comparison table for this Group

Group 3 BOM Table (RFAB/Process migration plus BOM update)

	Current	additional
Bond Wire composition/diameter	Cu, 0.96	Cu, 0.8

With these changes, the probe step (currently SFAB) will be removed from the process flow. No changes in final test sites for any of the below devices.

Qual details are provided in the Qual Data Section.

Reason for Change:

These changes are part of our multiyear plan to transition products from our 150-milimeter factories to newer, more efficient manufacturing processes and technologies, underscoring our commitment to product longevity and supply continuity.

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

Impact on Environmental Ratings:

Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.

RoHS	REACH	Green Status	IEC 62474
No Change	No Change	No Change	No Change

Changes to product identification resulting from this PCN:

Fab Site Information:

	Chip Site	Chip Site	
Chip Site	Origin Code	Country Code	Chip Site City
-	(20L)	(21L)	
SH-BIP-1	SHE	USA	Sherman
RFAB	RFB	USA	Richardson

Die Rev:

Current	New
Die Rev [2P]	Die Rev [2P]
A.M	A

Sample product shipping label (not actual product label)





(1P) \$N74L\$07N\$R (Q) 2000 (D) 0336 (31T)LOT: 3959047MLA (4W) TKY(1T) 7523483\$12 (P) (P) (P) (V) 0033917

(2P) REV: (20L) CSO: SHE (22L) ASO: MLA (23L) ACO: MYS

Product Affected:

Group 1 Device List (RFAB/Process migration, die change only):

SN74AC240QPWRQ1 | SN74AC240QPWRSV | SN74ACT244IPWRG4Q1 | SN74ACT244IPWRQ1

Group 3 Device List (RFAB/Process migration plus BOM update):

SN74AHC595QPWRQ1

For alternate parts with similar or improved performance, please visit the product page on $\overline{\text{TI.com}}$

R-CHG-2402-034 Q100

Automotive Qualification Summary (As per AEC-Q100 Rev. H and JEDEC Guidelines)

Gatorade BD10P5 PCN - 16 PW (1Q23) in MLA Automotive device Approve Date 18-March-2024

Product Attributes

Attributes	Qual Device:	QBS Process Reference:	QBS Package Reference:	QBS Product Reference:	
JII SELECTION AND ADDRESS OF THE PERSON AND	SN74AHC595QPWRQ1	SN74HCS74QPWRQ1	TXS0104EQPWRQ1	SN74LV8T594QPWRQ1	
Automotive Grade Level	Grade 1	Grade 1	Grade 1	Grade 1	
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125	-40 to 125	
Product Function	Logic	Logic	Logic	Logic	
Wafer Fab Supplier	RFAB	RFAB	RFAB	RFAB	
Assembly Site	MLA	MLA	MLA	MLA	
Package Group	TSSOP	TSSOP	TSSOP	TSSOP	
Package Designator	PW	PW	PW	PW	
Pin Count	16	14	14	16	

- QBS: Qual By Similarity
 Qual Device SN74AHC595QPWRQ1 is qualified at MSL1 260C

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Spec	Min Lot	SS/	Test Name	Condition	Duration	Qual Device:	QBS Process Reference:	QBS Package Reference:	QBS Product Reference:
			Qty	Lot				SN74AHC595QPWRQ1	SN74HCS74QPWRQ1	TXS0104EQPWRQ1	SN74LV8T594QPWRQ1
Test Group	A - Acc	elerated Enviror	ment Si	ress Te	sts						
PC	A1	JEDEC J- STD-020 JESD22- A113	3	77	Preconditioning	MSL1 260C		÷	3/0/0	3/0/0	1/0/0
HAST	A2	JEDEC JESD22- A110	3	77	Biased HAST	130C/85%RH	96 Hours		3/231/0	1/77/0	1/77/0
AC/UHAST	A3	JEDEC JESD22- A102/JEDEC JESD22- A118	3	77	Autoclave	121C/15psig	96 Hours		3/231/0		
AC/UHAST	A3	JEDEC JESD22- A102/JEDEC JESD22- A118	3	77	Unbiased HAST	130C/85%RH	96 Hours			1/77/0	1/77/0
тс	A4	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles		3/231/0	3/231/0	1/77/0
TC-BP	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull		-			1/5/0	1/5/0
HTSL	A6	JEDEC JESD22- A103	1	45	High Temperature Storage Life	150C	1000 Hours		3/135/0	-	
HTSL	A6	JEDEC JESD22- A103	1	45	High Temperature Storage Life	175C	500 Hours		-	1/45/0	1/45/0
Test Group	B - Acc	elerated Lifetime	Simula	tion Tes	ts						
HTOL	B1	JEDEC JESD22- A108	3	77	Life Test	125C	1000 Hours	-	3/231/0	1/77/0	1/77/0
ELFR	B2	AEC Q100- 008	3	800	Early Life Failure Rate	125C	48 Hours	*	3/2400/0		

Туре	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: SN74AHC595QPWRQ1	QBS Process Reference: SN74HCS74QPWRQ1	QBS Package Reference: <u>TXS0104EQPWRQ1</u>	QBS Product Reference: SN74LV8T594QPWRQ1
WBS	C1	AEC Q100- 001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires		3/90/0	1/30/0	1/30/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires		3/90/0	1/30/0	1/30/0
SD	C3	JEDEC J- STD-002	1	15	PB Solderability	>95% Lead Coverage	-		1/15/0	-	
SD	СЗ	JEDEC J- STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-		1/15/0	-	
PD	C4	JEDEC JESD22- B100 and B108	3	10	Physical Dimensions	Cpk>1.67	-	-	3/30/0	1/10/0	1/10/0
Test Group	D - Die F	abrication Relia	ıbility Te	sts							
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35		-	Time Dependent Dielectric Breakdown		-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28		-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
ВТІ	D4	-	-	-	Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Test Group	E - Elect	rical Verification	n Tests								
ESD	E2	AEC Q100- 002	1	3	ESD HBM		2000 Volts	1/3/0	1/3/0	1/3/0	1/3/0
ESD	E3	AEC Q100- 011	1	3	ESD CDM	-	500 Volts	1/3/0	1/3/0	1/3/0	1/3/0
Туре	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: SN74AHC595QPWRQ1	QBS Process Reference: SN74HCS74QPWRQ1	QBS Package Reference: <u>TXS0104EQPWRQ1</u>	QBS Product Reference: SN74LV8T594QPWRQ1
LU	E4	AEC Q100- 004	1	6	Latch-Up	Per AEC Q100-004	-	1/6/0	1/6/0	1/6/0	1/6/0
ED	E5	AEC Q100- 009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	1/30/0	3/90/0	3/90/0	3/90/0

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/Ik Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
 The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/Ik Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40C to +150C

- Grade 1 (or Q): -40C to +125C
- Grade 2 (or T): -40C to +105C
- Grade 3 (or I): -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold : HTOL, ED
- Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- · Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/

TI Qualification ID: R-CHG-2402-034

R-CHG-2402-034 Q006

Automotive Qualification Summary (As per AEC and JEDEC Guidelines)

Q006 TSSOP at MLA Approve Date 19-MARCH -2024

Attributes	Qual Device:	QBS Process Reference:	QBS Package Reference:
Authbuics	SN74AHC595QPWRQ1	SN74HCS74QPWRQ1	TXS0104EQPWRQ1
Automotive Grade Level	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125
Product Function	Logic	Logic	Logic
Wafer Fab Supplier	RFAB	RFAB	RFAB
Assembly Site	MLA	MLA	MLA
Package Group	TSSOP	TSSOP	TSSOP
Package Designator	PW	PW	PW
Pin Count	16	14	14

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: SN74AHC595QPWRQ1	QBS Reference: SN74HCS74QPWRQ1	QBS Reference: TXS0104EQPWRQ1
Test G	roup A - /	Accelerated E	nvironm	ent Stre	ss Tests					
PC	A1	JEDEC J- STD-020 JESD22- A113	3	77	Preconditioning	MSL1 260C	-	-	3/0/0	3/0/0
PC	A1.1	-	3	22	SAM Precon Pre	Review for delamination	-	-	3/66/0	3/66/0
PC	A1.2	-	3	22	SAM Precon Post	Review for delamination	-	-	3/66/0	3/66/0
HAST	A2.1	JEDEC JESD22- A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	3/231/0	-
HAST	A2.1.2	-	3	1	Cross Section, post bHAST, 1X	Post stress cross section	Completed	-	3/3/0	-
HAST	A2.1.3	-	3	3	Wire Bond Shear, post bHAST, 1X	Post stress	-	-	3/9/0	-
HAST	A2.1.4	-	3	3	Bond Pull over Stitch, post bHAST, 1X	Post stress	-	-	3/9/0	-
HAST	A2.1.5	-	3	3	Bond Pull over Ball, post bHAST, 1X	Post stress	-	-	3/9/0	-
HAST	A2.2	JEDEC JESD22- A110	3	70	Biased HAST	130C/85%RH	192 Hours	-	3/231/0	-
HAST	A2.2.1	-	3	22	SAM Analysis, post bHAST 2X	Review for delamination	Completed	-	3/66/0	-
HAST	A2.2.2	-	3	1	Cross Section, post bHAST, 2X	Post stress cross section	Completed	-	3/3/0	-
HAST	A2.2.3	-	3	3	Wire Bond Shear, post bHAST, 2X	Post stress	-	-	3/9/0	-

Туре	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: SN74AHC595QPWRQ1	QBS Reference: SN74HCS74QPWRQ1	QBS Reference: TXS0104EQPWRQ1
HAST	A2.2.4	-	3	3	Bond Pull over Stitch, post bHAST, 2X	Post stress	-	-	3/9/0	-
HAST	A2.2.5	-	3	3	Bond Pull over Ball, post bHAST, 2X	Post stress	-	-	3/9/0	-
тс	A4.1	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	-	3/231/0	3/231/0
тс	A4.1.1	-	3	22	SAM Analysis, post TC 1X	Review for delamination	Completed	-	3/66/0	3/66/0
тс	A4.1.2	-	3	1	Cross Section, post TC, 1X	Post stress cross section	Completed	-	3/3/0	3/3/0
тс	A4.1.3	-	3	3	Wire Bond Shear, post TC, 1X	Post stress	-	-	3/9/0	3/9/0
тс	A4.1.4	-	3	3	Bond Pull over Stitch, post TC, 1X	Post stress	-	-	3/9/0	3/9/0
тс	A4.1.5	-	3	3	Bond Pull over Ball, post TC, 1X	Post stress	-	-	3/9/0	3/9/0
тс	A4.2	JEDEC JESD22- A104 and Appendix 3	3	70	Temperature Cycle	-65C/150C	1000 Cycles	-	3/231/0	3/231/0
тс	A4.2.1	-	3	22	SAM Analysis, post TC, 2X	Review for delamination	Completed	-	3/66/0	3/66/0
тс	A4.2.2	-	3	1	Cross Section, post TC, 2X	Post stress cross section	Completed	-	3/3/0	3/3/0
тс	A4.2.3	-	3	3	Wire Bond Shear, post TC, 2X	Post stress	-	-	3/9/0	3/9/0

Туре	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: SN74AHC595QPWRQ1	QBS Reference: SN74HCS74QPWRQ1	QBS Reference: TXS0104EQPWRQ
TC	A4.2.4	-	3	3	Bond Pull over Stitch, post TC, 2X	Post stress	-	-	3/9/0	3/9/0
тс	A4.2.5	-	3	3	Bond Pull over Ball, post TC, 2X	Post stress	-	-	3/9/0	3/9/0
HTSL	A6.1	JEDEC JESD22- A103	3	45	High Temperature Storage Life	150C	1000 Hours	-	3/135/0	-
HTSL	A6.1.1	-	3	1	Cross Section, post HTSL, 1X	Post stress cross section	Completed	-	3/3/0	-
HTSL	A6.2	JEDEC JESD22- A103	3	44	High Temperature Storage Life	150C	2000 Hours	-	3/135/0	-
HTSL	A6.2.1	-	3	1	Cross Section, post HTSL, 2X	Post stress cross section	Completed	-	3/3/0	-
Test G	roup C - F	Package Asse	mbly Int	egrity Te	sts					
WBS	C1	AEC Q100- 001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	-	3/90/0	1/30/0
WBP	C2	MIL- STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	-	3/90/0	1/30/0

- QBS: Qual By Similarity
- Qual Device SN74AHC595QPWRQ1 is qualified at MSL1 260C
- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

Ambient Operating Temperature by Automotive Grade Level:

- Grade 0 (or E): -40C to +150C
- Grade 1 (or Q): -40C to +125C
- Grade 2 (or T): -40C to +105C
- Grade 3 (or I): -40C to +85C

E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

- Room/Hot/Cold : HTOL, ED
- Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU
- Room : AC/uHAST

Quality and Environmental data is available at Tl's external Web site: http://www.ti.com/

TI Qualification ID: R-CHG-2402-034

R-CHG-2306-038 Q100

Automotive Qualification Summary (As per AEC-Q100 Rev. H and JEDEC Guidelines)

BD16_20PW_Q1_MLA Approve Date 15-FEBRUARY -2024

Product Attributes

Attributes	Qual Device:	Qual Device:	Qual Device:	QBS Package, Process Reference:	QBS Package Reference:	QBS Package Reference:	QBS Product Reference:	QBS Product Reference:
Attributes	SN74AC240QPWRQ1	SN74ACT244IPWRG4Q1	SN74ACT244IPWRQ1	SN74HCS74QPWRQ1	SN74LV595AQPWRQ1	SN74LV8T245QPWRQ1	SN74AC244QWRKSRQ1	SN74AC244QPWRQ1
Automotive Grade Level	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125
Product Function	Logic	Logic	Logic	Logic	Logic	Logic	Logic	Logic
Wafer Fab Supplier	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB	RFAB
Assembly Site	MLA	MLA	MLA	MLA	MLA	MLA	CDAT	MLA
Package Group	TSSOP	TSSOP	TSSOP	TSSOP	TSSOP	TSSOP	QFN	TSSOP
Package Designator	PW	PW	PW	PW	PW	PW	RKS	PW
Pin Count	20	20	20	14	16	20	20	20

- QBS: Qual By Similarity
 Qual Device SN74AC24QPWRQ1 is qualified at MSL1 260C
 Qual Device SN74AC744PWRG4Q1 is qualified at MSL1 260C
 Qual Device SN74AC744PWRQ1 is qualified at MSL1 260C

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре		Test Spec	Min Lot Qty	SS/ Lot	Test Name	Condition	Duration	Qual Device: SN74AC240QPWRQ1	Qual Device: SN74ACT244IPWRG4Q1	Qual Device: SN74ACT244IPWRQ1	QBS Package, Process Reference: SN74HCS74QPWRQ1	QBS Package Reference: SN74LV595AQPWRQ1	QBS Package Reference: SN74LV8T245QPWRQ1	QBS Product Reference: SN74AC244QWRKSRQ1	QBS Product Reference: SN74AC244QPWRQ1
Test Group A - Accelerated Environment Stress Tests															
PC	A1	JEDEC J- STD-020 JESD22- A113	3	77	Preconditioning	MSL1 260C					3/0/0	1/0/0	10/0	1/0/0	1/0/0
HAST	A2	JEDEC JESD22- A110	3	77	Biased HAST	130C/85%RH	96 Hours				3/231/0	1/77/0	1/77/0	1/77/0	1/77/0
ACJUHAST	A3	JEDEC JESD22- A102/JEDEC JESD22- A118	3	77	Autoclave	121C/15psig	96 Hours				3/231/0				
ACJUHAST	A3	JEDEC JESD22- A102/JEDEC JESD22- A118	3	77	Unbiased HAST	130C/85%RH	96 Hours		-			1/77/0	1/77/0	1/77/0	1/77/0

Туре		Test Spec	Min Lot Qty	SS/ Lot	Test Name	Condition	Duration	Qual Device: SN74AC240QPWRQ1	Qual Device: SN74ACT244IPWRG4Q1	Qual Device: SN74ACT244IPWRQ1	QBS Package, Process Reference: SN74HCS74QPWRQ1	QBS Package Reference: SN74LV595AQPWRQ1	QBS Package Reference: SN74LV8T245QPWRQ1	QBS Product Reference: SN74AC244QWRKSRQ1	QBS Product Reference: SN74AC244QPWRQ1
тс	A4	JEDEC JESD22- A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles				3/231/0	1/77/0	1/77/0	1/77/0	1/77/0
TC-BP	A4	ML-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull							1/5/0		1/5/0	1/5/0
TC-SAM	A4		3	3	Post TC SAM	<50% delamination								1/12/0	
HTSL	A6	JEDEC JESD22- A103	1	45	High Temperature Storage Life	150C	1000 Hours				3/135/0				-
HTSL	A6	JEDEC JESD22- A103	1	45	High Temperature Storage Life	175C	500 Hours			-	-	1/45/0	1/45/0	1/45/0	1/45/0
Test Group	- Acce		Simula	tion Tes	ts										
HTOL	B1	JEDEC JESD22- A108	3	77	Life Test	125C	1000 Hours				3/231/0				1/77/0
HTOL	B1	JEDEC JESD22- A108	3	77	Life Test	150C	300 Hours			-	-	1/77/0	1/77/0	1/77/0	-
ELFR	B2	AEC Q100- 008	3	800	Early Life Failure Rate	125C	48 Hours				3/2400/0				-
Test Group	- Pack	age Assembly I	integrity	Tests											
WBS	C1	AEC Q100- 001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires				3/90/0	1/30/0	1/30/0	1/30/0	1/30/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires				3/90/0	1/30/0	1/30/0	1/30/0	1/30/0
SD	СЗ	JEDEC J- STD-002	1	15	PB Solderability	>95% Lead Coverage					1/15/0	1/15/0			
SD	C3	JEDEC J- STD-002	1	15	PB-Free Solderability	>95% Lead Coverage					1/15/0	1/15/0			
PD	C4	JEDEC JESD22- B100 and B108	3	10	Physical Dimensions	Cpk>1.67					3/30/0	1/10/0	1/10/0	1/10/0	1/10/0
Test Group I	o - Die Fi	abrication Relia	ibility Te	sts											
EM	D1	JESD61			Electromigration			Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDB	D2	JESD35			Time Dependent Dielectric Breakdown			Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28			Hot Carrier Injection			Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
вп	D4				Bias Temperature Instability			Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5				Stress Migration			Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Test Group	E - Electr	rical Verification	n Tests												
ESD	E2	AEC Q100- 002	1	3	ESD HBM		2000 Volts	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0
ESD	E3	AEC Q100- 011	1	3	ESD CDM		500 Volts	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0	1/3/0
Туре		Test Spec	Min Lot Qty	SSI	Test Name	Condition	Duration	Qual Device: SN74AC240QPWRQ1	Qual Device: SN74ACT244IPWRG4Q1	Qual Device:	QBS Package, Process Reference: SN74HCS74QPWRQ1	QBS Package Reference: SN74LV595AQPWRQ1	QBS Package Reference: SN74LV8T245QPWRQ1	QBS Product Reference: SN74AC244QWRKSRQ1	QBS Product Reference: SN74AC244QPWRQ1
LU	E4	AEC Q100- 004	1	6	Latch-Up	Per AEC Q100-004	-	1/6/0	1/6/0	1/6/0	1/6/0	1/6/0	1/6/0	1/6/0	1/6/0
ED	E5	AEC Q100- 009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	1/30/0	1/30/0	1/30/0	3/90/0	3/90/0	3/90/0	2/60/0	1/30/0

- rreconditioning was performed for Autoclave, Unbiased HAST, ThBBiased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
 The following are equivalent HTCs, options based on an activation energy of 0.7 kV: 125/CIA Hours, 140/CI460 Hours, 150/CI900 Hours, and 155/CI40 Hours
 The following an equivalent HTCs, options based on an activation energy of 0.7 kV: 155/CIA Hours, and 170/CI420 Hours
 The following are equivalent Remp Cycle options pard on an activation energy of 0.7 kV: 155/CIA Hours, and 170/CI420 Hours
 The following are equivalent Remp Cycle options per JESCH7: 55/CI25C/700 Cycles and 45/CI350C5500 Cycles

- Room/Hos/Cold : HTOL, ED
 Room/Hos : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

alification ID: R-CHG-2306-038

ZVEI Ids: SEM-DE-03, SEM-PW-02, SEM-PW-09, SEM-PW-13, SEM-TF-01, SEM-PA-08

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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