REVISION E

RECOMMENDED PCB LAYOUT FOR UEC5-XXX-2-XXX-D-RA-1 & UCC8-XXX-X-XXX-X-1-A

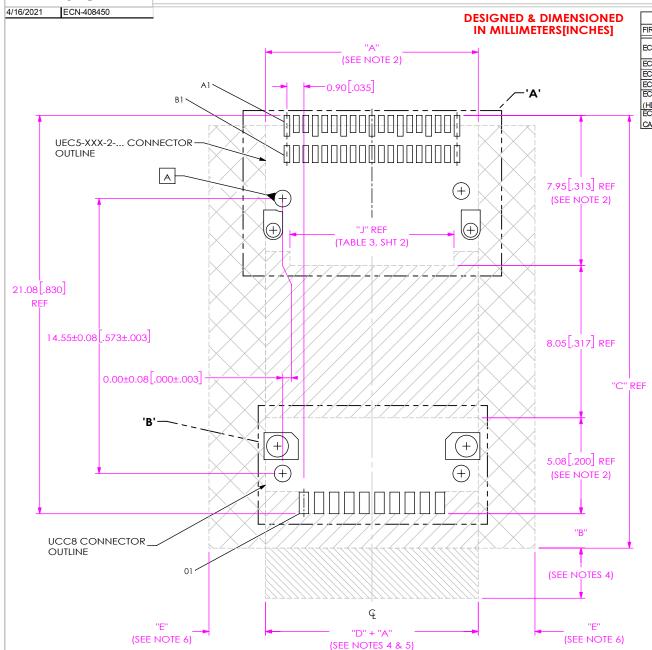


TABLE2								
FIREFLY MATE	OPTIONS	"B"	"C"	"D"	į			
ECUO (OPTICAL)	HEATSINK: -1 & -3 (FLAT)	4.02 [0.158]	25.10 [0.988]	0.00 [0.000]	0.00 [0.000]			
ECOO (OPTICAL)	HEATSINK: -2 (PIN-FIN)	2.60 [0.102]	23.68 [0.932]	0.00 [0.000]	0.00 [0.000]			
ECUE-E (COPPER)	CABLE: -T1-PX & -T2-PX	6.66 [0.262]	27.74 [1.092]	0.00 [0.000]	N/A			
ECUE-E (COPPER)	CABLE: -T2-AX	8.73 [0.344]	29.81 [1.174]	0.00 [0.000]	N/A			
ECUE (COPPER)	N/A	5.41 [0.213]	26.49 [1.043]	0.00 [0.000]	N/A			
ECUE (COPPER)	TEST CABLE	5.41 [0.213]	26.49 [1.043]	2.51 [0.099]	N/A			
(HDR-179381-XX)	TEST GABLE	3.41 [0.213]	20.43 [1.043]	2.51 [0.055]	IVA			
ECUO WHEN USING	,	5.50 [0.217]						
CAT-IN-ECUO-XX TOOL	O-XX TOOL SEE ECUO OPTIONS							

TABLE 1							
No O	"A"						
UEC5 -2	UCC8						
-019	-010	11.25 [0.443]					
*-025	*-014	14.25 [0.561]					
(*= NOT TOOLED)							

FIG 1 UEC5-019-2-XXX-D-RA-1 SHOWN UCC8-010-1-XXX-S-1-A SHOWN

1. ALL DIMENSIONS ARE SYMMETRIC ABOUT THE CENTERLINE, EXCEPT AS SHOWN.
2. CONNECTOR ENVELOPE DIMENSIONS, OTHER COMPONENTS PLACED ON PCB SHALL AVOID THIS ZONE.

3. NOTE DELETED.

4. RECOMMENDED KEEPOUT REGION FOR ROW-TO-ROW LAYOUT OF ECUO AND ECUE. THIS KEEPOUT REGION IS APPLICATION DEPENDENT. FIBER TYPE, HEATSINK STYLE, AND OTHER BOARD COMPONENTS AND HARDWARE SHOULD BE FACTORED IN TO DETERMINE THE APPROPRIATE SPACING FOR ROW-TO-ROW LAYOUT OF ECUO AND ECUE.

CONSULT SAMTEC ENGINEERING FOR MORE INFORMATION AND REFERENCE PART #: APP-ECUX-STACKING.
5. AREA SIZE AND MAXIMUM COMPONENT HEIGHT IN THIS REGION LIMITED TO ALLOW FOR FLYOVER CABLE OR FIBER CLEARANCE. 6. THIS AREA MUST BE VOID OF ANY COMPONENTS OR OBSTRUCTIONS TO ALLOW CAT-IN-ECUO-XX TOOL TO WORK PROPERLY WITHOUT DAMAGE TO CUSTOMER BOARD. USE OF THIS TOOL ALSO INCREASES MULTIPLE FIREFLY SIDE-BY-SIDE SPACING; SEE APPLICATION SPEC IN FIREFLY MANUAL.

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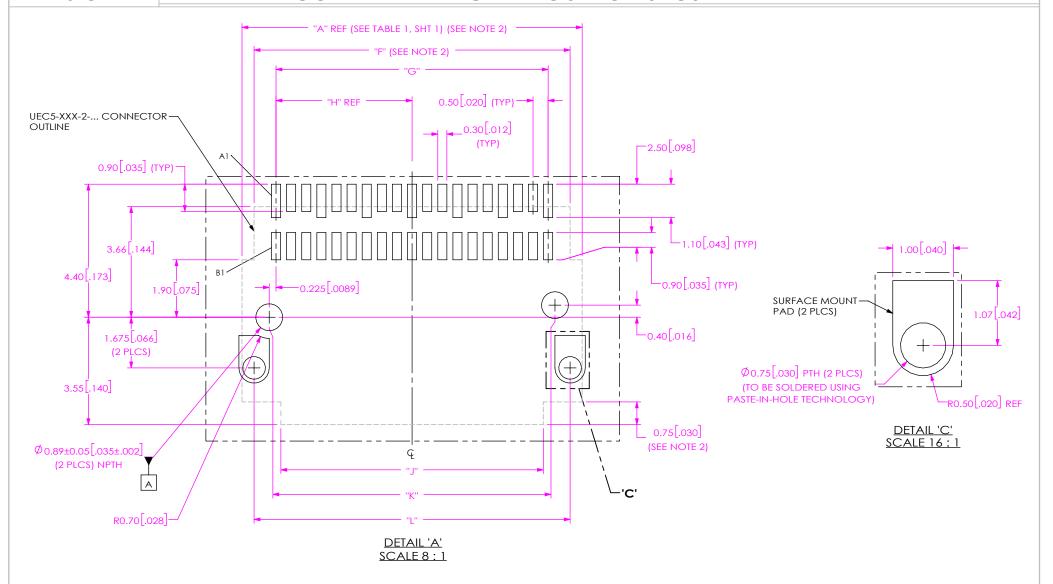
PROPRIETARY NOTE



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REVISION E

RECOMMENDED PCB LAYOUT FOR UEC5-XXX-2-XXX-D-RA-1



UEC5-XXX-2-X-D-RA-1 SIGNAL MAPPING																		
A1	A2	A3	A4	A5	A6	A7	A8	A9	A10	A11	A12	A13	A14	A15	A16	A17	A18	A19
G	PA	IR	G	PA	IR	G	PA	IR	G	PA	IR	G	PA	IR	G	PA	IR	G
G	PA	IR	G	PA	IR	G	PA	IR	G	PA	IR	G	PA	IR	G	PA	IR	G
B1	B2	В3	B4	В5	В6	В7	В8	В9	B10	B11	B12	B13	B14	B15	B16	B17	B18	B19
ALL CROWN DIVIS IN DOWN A ADD COMMON																		

ALL GROUND PINS IN ROW A ARE COMMON.

TABLE 3									
	No OF POS	"="	"G"	"H"	"ل"	"K"	===	"Q"	
	UEC5	'	0	"	5	K	١		
	-019	10.45 [0.411]	9.00 [0.354]	4.500 [0.1772]	8.68 [0.342]	9.45 [0.372]	10.45 [0.411]	11.20 [0.441]	
	*-025	13.45 [0.530]	12.00 [0.472]	6.000 [0.2362]	11.68 [0.460]	12.45 [0.490]	13.45 [0.530]	14.20 [0.559]	
ľ	(*= NOT TOO	LED)							

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RECOMMENDED PCB LAYOUT FOR UCC8-XXX-X-XXX-X-1-A

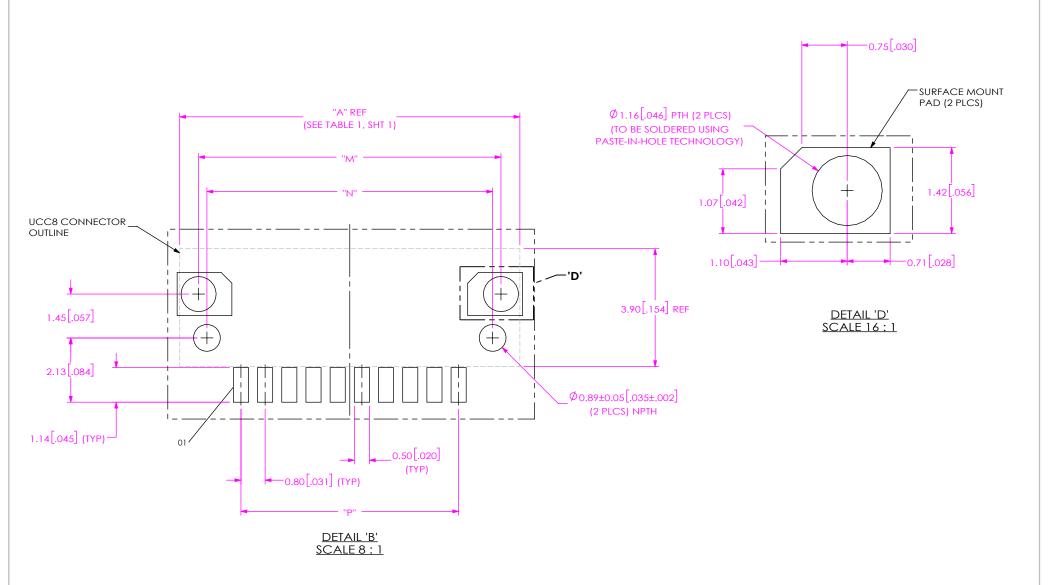


TABLE 4									
No OF POS	"M"	"N"	ים"	"R"					
UCC8	IVI	14	F	IX.					
-010	10.00 [0.394]	9.45 [0.372]	7.20 [0.283]	11.61 [0.457]					
*-014	13.00 [0.512]	12.45 [0.490]	10.20 [0.402]	14.61 [0.575]					
(*= NOT TOOLED)									

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STENCIL TO BE 0.13[.005] THICK.

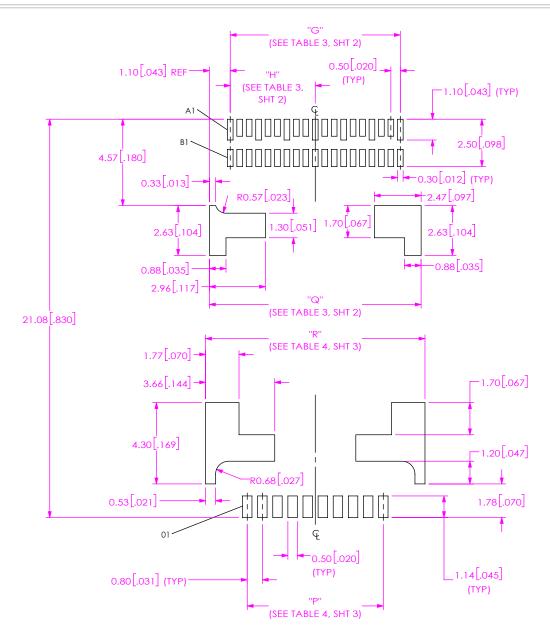


FIG 2 UEC5-019-2-XXX-D-RA-1 STENCIL SHOWN UCC8-010-1-XXX-S-1-A STENCIL SHOWN

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