PCN Number:		20240227008.0		PCN Date:	February 27, 2024
Title:	Datasheet for TMS320F2837xD, TMS320F2837xS, and TMS320F2807x				
<b>Customer Contact:</b>		Change Management team	Dept:	Quality Services	
Change Type:		Electrical Specification			

## **PCN Details**

## **Description of Change:**

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below.

The following change history provides further details.



TMS320F28379D, TMS320F28379D-Q1, TMS320F28378D, TMS320F28377D TMS320F28377D-Q1, TMS320F28376D, TMS320F28375D, TMS320F28374D SPRS880P - DECEMBER 2013 - REVISED FEBRUARY 2024

_	INSTRUMENTS SPRS880P – DECEMBER 2013 – REVISED FEBRUARY	202
		age
•	Changed document title from TMS320F2837xD Dual-Core Microcontrollers to TMS320F2837xD Dual-Core	е
	Real-Time Microcontrollers	
•	Global: Changed the title of the errata from TMS320F2837xD Dual-Core MCUs Silicon Errata to	
	TMS320F2837xD Dual-Core Real-Time MCUs Silicon Errata. Changed the title of the Technical Reference	9
	Manual from TMS320F2837xD Dual-Core Microcontrollers Technical Reference Manual to TMS320F2837	χD
	Dual-Core Real-Time Microcontrollers Technical Reference Manual	٠ '
•	Description section: Updated section	2
•	Package Information table: Changed title of Device Information table to Package Information. Updated table and footnotes	
•	Device Comparison table: Updated Serial Communications Interface (SCI) – Type 0 with (UART Compatible)	
•	Pin Configuration and Functions section: Changed section title from Terminal Configuration and Functions Pin Configuration and Functions	9
•	Signal Descriptions table: Updated DESCRIPTION column of TRST and VDD. Updated PTP PIN NO. colu	
	and PZP PIN NO. column of VSS	
•	Input X-BAR figure: Updated figure	
•	ESD Ratings – Commercial table: Updated part numbers.	
•	ESD Ratings – Automotive table: Updated part numbers	
•	Device Current Consumption at 200-MHz SYSCLK table: Added values for RESET MODE	50
•	Electrical Characteristics table: Moved parametric value of V <sub>HYSTERESIS</sub> (150 mV) from TYP column to MIN column	
•	Power-on Reset figure: Updated figure	60
•	Clocking System figure: Updated figure	
•	XTAL Oscillator Characteristics section: Added section	
•	XTAL Oscillator section: Changed section title from Crystal Oscillator to XTAL Oscillator. Updated section	6
•	Crystal Oscillator Electrical Characteristics table: Updated table	
•	Negative Resistance Variation at 10 MHz figure: Added figure	
•	Negative Resistance Variation at 20 MHz figure: Added figure	
•	Flash Parameters table: Updated table	
•	RAM Specifications section: Added section	7
•	ROM Specifications section: Added section	76
•	EMIF Asynchronous Memory Switching Characteristics table: Updated Parameters 3, 10, 15, and 24. Adde	
	"Maximum wait time-out condition" footnote	
•	Analog Subsystem Block Diagram (100-Pin PZP) figure: Updated figure	10
é	ADC Characteristics (16-Bit Differential Mode) table: Updated TYP values of SNR, THD, SFDR, SINAD, and ENOB	0
	ADC Characteristics (12-Bit Single-Ended Mode) table: Updated TYP values of SNR, THD, SFDR, SINAD, and ENOB	11
	Single-Ended Input Model Parameters section: Updated "These input models should be used along with inctual signal source impedance" paragraph11	
	ADC Timings for 12-Bit Mode figure: Updated figure	
	Comparator Electrical Characteristics table: Added MIN and MAX Hysteresis values. Added Power Supply	
	Rejection Ratio (PSRR)	23

:	CMPSS DAC Static Electrical Characteristics section: Added "Figures not drawn to scale" Note  CMPSS DAC Dynamic Error section: Added section.	131
:	Synchronization Chain Architecture figure: Updated figure.  SDFM Timing Requirements When Using Asynchronous GPIO (ASYNC) Option section: Updated W. about SDFM Manchester Mode (Mode 2)	ARNING
•	I2C Electrical Data and Timing section: Added "To meet all of the I2C protocol timing specifications, module clock must be configured in the range from 7 MHz to 12 MHz" Note.	
•	I2C Timing Requirements table: Added footnote.  I2C Timing Diagram section: Added section title.	
	I2C Timing Diagram section: Removed duplicate "To meet all of the I2C protocol timing specification	s, the I2C
	module clock (Fmod) must be configured from 7 MHz to 12 MHz." Note. This Note is now in the I2C Data and Timing section	Electrical
•	Overview section: Updated section	185
•	EMIF Chip Select Memory Map table: Updated SIZE for "EMIF2_CS0n - Data".	
•	Peripheral Registers Memory Map section: Added "None of the device peripherals have program bu access" Note	
•	Peripheral Registers Memory Map table: Added CLB registers	
•	Applications, Implementation, and Layout section: Updated section	
•	Tools and Software section: Added C2000 Third-party search tool. Updated Training section	232
	INSTRUMENTS  SPRS881K - AUGUST 2014 - REVISED F  hanges from February 1, 2021 to February 21, 2024 (from Revision J (February 2021) to	
	evision K (February 2024))	Page
•	Changed document title from TMS320F2837xS Microcontrollers to TMS320F2837xS Real-Time Microcontrollers	4
•	Global: Changed the title of the errata from TMS320F2837xS MCUs Silicon Errata to TMS320F283 Time MCUs Silicon Errata. Changed the title of the Technical Reference Manual from TMS320F283 Microcontrollers Technical Reference Manual to TMS320F2837xS Real-Time Microcontrollers Technical	7xS Real- 7xS
•	Global: Changed the title of the errata from TMS320F2837xS MCUs Silicon Errata to TMS320F283 Time MCUs Silicon Errata. Changed the title of the Technical Reference Manual from TMS320F283 Microcontrollers Technical Reference Manual to TMS320F2837xS Real-Time Microcontrollers Technical Reference Manual	7xS Real- 7xS nical 1
	Global: Changed the title of the errata from TMS320F2837xS MCUs Silicon Errata to TMS320F283 Time MCUs Silicon Errata. Changed the title of the Technical Reference Manual from TMS320F283 Microcontrollers Technical Reference Manual to TMS320F2837xS Real-Time Microcontrollers Technical Reference Manual  Description section: Updated section.	7xS Real- 7xS nical 1
:	Global: Changed the title of the errata from TMS320F2837xS MCUs Silicon Errata to TMS320F283 Time MCUs Silicon Errata. Changed the title of the Technical Reference Manual from TMS320F283 Microcontrollers Technical Reference Manual to TMS320F2837xS Real-Time Microcontrollers Technical Reference Manual.  Description section: Updated section.  Package Information table: Changed title of Device Information table to Package Information. Updat and footnotes.	7xS Real- 7xS nical 1 ed table
:	Global: Changed the title of the errata from TMS320F2837xS MCUs Silicon Errata to TMS320F283 Time MCUs Silicon Errata. Changed the title of the Technical Reference Manual from TMS320F283 Microcontrollers Technical Reference Manual to TMS320F2837xS Real-Time Microcontrollers Technical Reference Manual  Description section: Updated section  Package Information table: Changed title of Device Information table to Package Information. Update and footnotes  Device Comparison table: Updated Serial Communications Interface (SCI) – Type 0 with (UART Compatible)	7xS Real- 7xS nical
:	Global: Changed the title of the errata from TMS320F2837xS MCUs Silicon Errata to TMS320F283 Time MCUs Silicon Errata. Changed the title of the Technical Reference Manual from TMS320F283 Microcontrollers Technical Reference Manual to TMS320F2837xS Real-Time Microcontrollers Technical Reference Manual	7xS Real- 7xS nical
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	Global: Changed the title of the errata from TMS320F2837xS MCUs Silicon Errata to TMS320F283 Time MCUs Silicon Errata. Changed the title of the Technical Reference Manual from TMS320F283 Microcontrollers Technical Reference Manual to TMS320F2837xS Real-Time Microcontrollers Technical Reference Manual.  Description section: Updated section.  Package Information table: Changed title of Device Information table to Package Information. Update and footnotes.  Device Comparison table: Updated Serial Communications Interface (SCI) – Type 0 with (UART Compatible).  Pin Configuration and Functions section: Changed section title from Terminal Configuration and Function Configuration and Functions.  Signal Descriptions table: Updated DESCRIPTION column of TRST and VDD. Updated PTP PIN Notand PZP PIN No. column of VSS.  Input X-BAR figure: Updated figure.  ESD Ratings – Commercial table: Updated part numbers.  ESD Ratings – Automotive table: Updated part numbers.  Device Current Consumption at 200-MHz SYSCLK table: Added values for RESET MODE.  Electrical Characteristics table: Moved parametric value of VHYSTERESIS (150 mV) from TYP column MIN column.	7xS Real- 7xS nical
	Global: Changed the title of the errata from TMS320F2837xS MCUs Silicon Errata to TMS320F283 Time MCUs Silicon Errata. Changed the title of the Technical Reference Manual from TMS320F283 Microcontrollers Technical Reference Manual to TMS320F2837xS Real-Time Microcontrollers Technical Reference Manual.  Description section: Updated section.  Package Information table: Changed title of Device Information table to Package Information. Update and footnotes.  Device Comparison table: Updated Serial Communications Interface (SCI) – Type 0 with (UART Compatible).  Pin Configuration and Functions section: Changed section title from Terminal Configuration and Functions.  Signal Descriptions table: Updated DESCRIPTION column of TRST and VDD. Updated PTP PIN No and PZP PIN NO. column of VSS.  Input X-BAR figure: Updated figure.  ESD Ratings – Commercial table: Updated part numbers.  ESD Ratings – Automotive table: Updated part numbers.  Device Current Consumption at 200-MHz SYSCLK table: Added values for RESET MODE.  Electrical Characteristics table: Moved parametric value of VHYSTERESIS (150 mV) from TYP column MIN column.  Power-on Reset figure: Updated figure.	7xS Real- 7xS nical
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	EMIF Asynchronous Memory Switching Characteristics table: Updated Parameters 3, 10, 15, and 24	
	"Maximum wait time-out condition" footnote	
•	Analog Subsystem Block Diagram (100-Pin PZP) figure: Updated figure.	
•	ADC Characteristics (16-Bit Differential Mode) table: Updated TYP values of SNR, THD, SFDR, SIN/ and ENOB	
•	ADC Characteristics (12-Bit Single-Ended Mode) table: Updated TYP values of SNR, THD, SFDR, S and ENOB	
•	Single-Ended Input Model Parameters section: Updated "These input models should be used along v	vith
	actual signal source impedance" paragraph	
•	ADC Timings for 12-Bit Mode figure: Updated figure	
•	Comparator Electrical Characteristics table: Added MIN and MAX Hysteresis values. Added Power S Rejection Ratio (PSRR)	
•	CMPSS DAC Static Electrical Characteristics section: Added "Figures not drawn to scale" Note	
•	CMPSS DAC Dynamic Error section: Added section	
•	Synchronization Chain Architecture figure: Updated figure	138
•	SDFM Timing Requirements When Using Asynchronous GPIO (ASYNC) Option section: Updated Williams	ARNING
	about SDFM Manchester Mode (Mode 2)	147
	I2C Electrical Data and Timing section: Added "To meet all of the I2C protocol timing specifications, the module clock must be configured in the range from 7 MHz to 12 MHz" Note	156 156
	I2C Timing Diagram section: Added section title.  I2C Timing Diagram section: Removed duplicate "To meet all of the I2C protocol timing specifications	
•	module clock (Fmod) must be configured from 7 MHz to 12 MHz." Note. This Note is now in the I2C E	Electrical
	Data and Timing section	
	Overview section: Updated section	
	EMIF Chip Select Memory Map table: Updated SIZE for "EMIF2_CS0n - Data".	
•	Peripheral Registers Memory Map section: Added "None of the device peripherals have program bus access" Note	
	Peripheral Registers Memory Map table: Added CLB registers	
	Applications, Implementation, and Layout section: Updated section	213
		213
	Applications, Implementation, and Layout section: Updated section	213 233 DF28075-Q1
- c	Applications, Implementation, and Layout section: Updated section	213 233 DF28075-Q1
- c	Applications, Implementation, and Layout section: Updated section	213 233 DF28075-Q1
- c	Applications, Implementation, and Layout section: Updated section	Page 1 x Real-7x cal
- c	Applications, Implementation, and Layout section: Updated section	Page 1 x Real-7x cal
- c	Applications, Implementation, and Layout section: Updated section	Page  Page
- c	Applications, Implementation, and Layout section: Updated section	Page  Page
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- c	Applications, Implementation, and Layout section: Updated section	Page Page
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- c	Applications, Implementation, and Layout section: Updated section	Page Page  Page  1 x Real- 7x cal
- c	Applications, Implementation, and Layout section: Updated section	Page Page  Page  1 x Real- 7x cal
- c	Applications, Implementation, and Layout section: Updated section	Page Page  Page  1 x Real- 7x cal

	Electrical Characteristics table: Moved parametric value of V <sub>HYSTERESIS</sub> (150 mV) from TYP column to	
	MIN column	43
	Internal 1.2-V VREG section: Updated section.	
	Power-on Reset figure: Updated figure.	
	Clocking System figure: Updated figure.	
	XTAL Oscillator Characteristics section: Added section.	
	XTAL Oscillator section: Changed section title from Crystal Oscillator to XTAL Oscillator. Updated section	
	Crystal Oscillator Electrical Characteristics table: Updated table	
	Negative Resistance Variation at 10 MHz figure: Added figure	
:	Negative Resistance Variation at 10 MHz figure: Added figure:	
•		
•	Flash Parameters table: Updated table	62
•	RAM Specifications section: Added section	
•	ROM Specifications section: Added section.	
•	EMIF Asynchronous Memory Switching Characteristics table: Updated Parameters 3, 10, 15, and 24. Ad	dded
	"Maximum wait time-out condition" footnote	
•	ADC Characteristics table: Updated TYP values of SNR, THD, SFDR, SINAD, and ENOB	
•	Single-Ended Input Model Parameters section: Updated "This input model should be used along with ac	
	signal source impedance" paragraph	
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	Rejection Ratio (PSRR)	
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•	Synchronization Chain Architecture figure: Updated figure	120
•	SDFM Timing Requirements When Using Asynchronous GPIO (ASYNC) Option section: Updated WAR	NING
	about SDFM Manchester Mode (Mode 2)	129
•	I2C Electrical Data and Timing section: Added "To meet all of the I2C protocol timing specifications, the	I2C
	module clock must be configured in the range from 7 MHz to 12 MHz " Note Note	138
•	I2C Timing Requirements table: Added footnote.	138
	IOO Tireles Discours as allow Added as allow Allies	400
•	I2C Timing Diagram section: Added section title.	
•	12C Timing Diagram section: Removed duplicate "To meet all of the I2C protocol timing specifications, the	
	module clock (Fmod) must be configured from 7 MHz to 12 MHz." Note. This Note is now in the I2C Elec	
	Data and Timing section	
•	Overview section: Updated section	162
•	Peripheral Registers Memory Map section: Added "None of the device peripherals have program bus	
	access" Note	
•	Peripheral Registers Memory Map table: Added CLB registers	
•	Applications, Implementation, and Layout section: Updated section	
•	Tools and Software section: Added C2000 Third-party search tool. Updated Training section	207

The datasheet number will be changing.

Device Family	Change From:	Change To:
TMS320F2837xD	SPRS8800	SPRS880P
TMS320F2837xS	SPRS881J	SPRS881K
TMS320F2807x	SPRS902J	SPRS902K

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/TMS320F28379D http://www.ti.com/product/TMS320F28379S

http://www.ti.com/product/TMS320F28076

# Reason\_for Change:

To accurately reflect device characteristics.

### Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device

## Changes to product identification resulting from this PCN:

None.

TOTAL				
Product Affected:				
TMS320F28374SZWTT	TMS320F28075PZPQ	TMS320F28375SZWTT	TMS320F28376SPZPS	
TMS320F28376SZWTT	TMS320F28374DZWTS	TMS320F28377DZWTT	TMS320F28377DPTPT	
TMS320F28377DZWTQ	TMS320F28375DZWTT	TMS320F28379DPTPQ	TMS320F28377DZWTS	
TMS320F28379DPTPS	TMS320F28375SZWTS	TMS320F28379SPZPT	TMS320F28377SZWTS	
TMS320F28379DPTPT	TMS320F28376DPTPS	TMS320F28075PTPS	TMS320F28378SPTPS	
TMS320F28379SPTPT	TMS320F28377DPTPQ	TMS320F2807NPTPT	TMS320F28379DZWTT	
TMS320F28075PTPT	TMS320F28377SPTPS	TMS320F28075PTPQR	TMS320F28379SZWTT	
TMS320F28374DPTPS	TMS320F28377SPZPS	TMS320F28375DZWTS	TMS320F28374SPZPS	
TMS320F28374DPTPT	TMS320F28377SZWTT	TMS320F28375SPZPQ	TMS320F28374SZWTS	
TMS320F28374SPTPS	TMS320F28377SPTPQ	TMS320F28376SPTPT	TMS320F28376DPTPT	
TMS320F28374SPTPSR	TMS320F28377SPZPQ	TMS320F28377DZWTQR	TMS320F28377SPZPT	
TMS320F28374SPZPT	TMS320F28378DPTPS	TMS320F28377SPTPT	TMS320F28379DZWTS	
TMS320F28375SPZPS	TMS320F28075PTPQ	TMS320F28379DZWTQR	TMS320F2837HDPTPT	
TMS320F28375SPZPQR	TMS320F28076PTPS	TMS320F28379SPTPS	TMS320F28374SPTPT	
TMS320F28376SPZPT	TMS320F28076PZPS	TMS320F28379SPZPS	TMS320F28374SZWTTR	
TMS320F28376SZWTS	TMS320F28374DZWTT	TMS320F28379SZWTS	TMS320F28375SPTPT	
TMS320F28377DPTPS	TMS320F28375DPTPS	TMS320F28375DPTPT	TMS320F28375SPZPT	
TMS320F28377SZWTQ	TMS320F28375SPTPS	TMS320F28375DPZPS	TMS320F28378SPZPS	
TMS320F28075PZPS	TMS320F28075PZPT			

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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