Notification Number:		20240213004.0				PCN Date:		Date:	February 14, 2024	
Title: TPS6594133A Firmware change										
Customer Contact:			Change Management team			Dept:		t:	Quality Services	
Change Type:										
Assembly Site				\boxtimes	Design			Wafer Bump Material		
Assembly Process					Data Sheet			Wafer B	ump Process	
Assembly Materials					Part number change			Wafer F	ab Site	
Mechanical Specification					Test Site			Wafer F	ab Materials	
Packing/Shipping/Labelin			3		Test Process			Wafer F	ab Process	
Notification Details										
Description of Change										

Description of Change:

This notification is to communicate the Firmware change for the TPS6594133A. Affected devices are listed in the Product Affected section of this document.

The firmware was updated as follows:

Watchdog enabled by default with 13 minute long window

Default GPIO9 function changed from GPIO to WD_DISABLE. GPIO9 starts as an input to set WD_PWRHOLD bit, then changes to GPIO output as was previously.

TO_ACTIVE sequence changed to 500us delay between LDO3 and BUCK5 LDO2 OV/UV Threshold changed from 5% to 10%

As a result, the Register NVM_CODE_2 was changed from 0x03 to 0x05.

No hardware external changes are required to support this update.

Reason for Change:

Improved device functionality

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

None.

Product Affected:

TPS6594133ARWERQ1

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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