

PCN Number:	20240122004.0	PCN Date:	January 31, 2024
Title:	Datasheet for BQ25628E and BQ25622E		
Customer Contact:	Change Management team	Dept:	Quality Services
Change Type:	Electrical Specification		

PCN Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



BQ25628E
SLUSFA4A – JUNE 2023 – REVISED JANUARY 2024

Changes from Revision * (July 2023) to Revision A (January 2024)	Page
• Added 5mA to 310mA, 5mA steps to Charge termination feature.....	1
• Changed t _{RST} to t _{QON_RST} in QON pin description.....	5
• Added PG pin description.....	5
• Added MAX value to V _{POORSRC}	8
• Changed IBAT_ADC LSB TYP value from 2mA to 4mA.....	8
• Deleted t _{VBUS_OVP_PROP} , t _{POORSRC_RETRY} , t _{POORSRC_RESTART} , t _{VBUS_PD} , t _{TERM_DGL} , t _{RECHG_DGL} , t _{PMID_OVP_PROP} and t _{PMID_OVP_FALL} specifications.....	15
• Added register conditions to test conditions for t _{TOP_OFF} specifications.....	15
• Added Figure 8-3 to TS Pin Thermistor Configuration.....	24
• Updated behavior of battery overcurrent protection in Section 8.3.9.1.2	29
• Deleted invalid reference in Section 8.3.9.2.1 to improve clarity.....	30
• Deleted invalid reference in Section 8.3.9.2.2 to improve clarity.....	30
• Deleted invalid reference in Section 8.3.9.2.4 to improve clarity.....	30
• Deleted invalid reference in Section 8.3.9.2.5 to improve clarity.....	30
• Deleted invalid reference in Section 8.3.9.2.6 to improve clarity.....	30
• Added I ² C address in Section 8.5.1.....	33
• Changed bit 5 to Reserved, ICHG Reset from X to Dh, POR from 1Ah to Dh, Range from 2h-4Ch to 1h-26h, and Bit Step from 2h to 1h in Register Fields and Descriptions for REG0x02_Charge_Current_Limit register.....	36
• Changed bit 3 to Reserved, IPRECHG Reset from 0xA to 5h, POR from Ah to 5h, Range from 2h-3Eh to 1h-1Fh, and Bit Step from 2h to 1h in Register Fields and Descriptions for REG0x10_Pre-charge_Control register.....	36
• Changed bit 2 to Reserved, ITERM Reset from 0xC to 6h, POR from Ch to 6h, Range from 2h-7Ch to 1h-3Eh, and Bit Step from 2h to 1h in Register Fields and Descriptions for REG0x12_Termination_Control register	36
• Changed I ² C address from 0x6B to 0x6A in Register Maps section.....	36

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• Changed VBUS_ADC range maximum from 111B6h to 11B6h in REG0x2C_VBUS_ADC Register Field Descriptions.....	36

The datasheet number will be changing.

Device Family	Change From:	Change To:
BQ25628E	SLUSFA4	SLUSFA4A
BQ25622E	SLUSFA3	SLUSFA3A

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/BQ25628E>

<http://www.ti.com/product/BQ25622E>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device

Changes to product identification resulting from this PCN:

None.

Product Affected:

BQ25628ERYKR	BQ25622ERYKR		
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For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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