

Product Discontinuation Notice for Virtex UltraScale+ High-Bandwidth Memory FPGAs

XCN23006 (v1.0) December 04, 2023

Product Discontinuation Notice

Overview

The purpose of this notification is to communicate that Advanced Micro Devices, Inc. is discontinuing Virtex™ UltraScale+™ HBM (high-bandwidth memory) FPGAs.

Description

AMD will be discontinuing Virtex UltraScale+ HBM FPGAs due to supplier sustainability reasons.

This notice should be used in conjunction with previous discontinuance notices, which can be found at Xilinx.com.

Products Affected

The products affected include all standard part numbers and specification control document (SCD) versions of the standard part numbers listed in this notice.

Table 1: Virtex UltraScale+ FPGA Products Affected

Part Number	Replacement Part
XCVU31P-2FSVH1924E	No direct replacement
XCVU31P-1FSVH1924E	No direct replacement
XCVU31P-3FSVH1924E	No direct replacement
XCVU31P-L2FSVH1924E	No direct replacement
XCVU33P-2FSVH2104E	No direct replacement
XCVU33P-3FSVH2104E	No direct replacement
XCVU33P-1FSVH2104E	No direct replacement
XCVU33P-L2FSVH2104E	No direct replacement
XCVU35P-1FSVH2104E	No direct replacement
XCVU35P-1FSVH2892E	No direct replacement
XCVU35P-2FSVH2104E	No direct replacement
XCVU35P-2FSVH2892E	No direct replacement
XCVU35P-3FSVH2892E	No direct replacement
XCVU35P-L2FSVH2104E	No direct replacement
XCVU35P-L2FSVH2892E	No direct replacement
XCVU35P-3FSVH2104E	No direct replacement
XCVU37P-1FSVH2892E	No direct replacement
XCVU37P-2FSVH2892E	No direct replacement
XCVU37P-3FSVH2892E	No direct replacement
XCVU37P-L2FSVH2892E	No direct replacement

Part Number	Replacement Part	
XQVU37P-1FSQH2892E	No direct replacement	
XQVU37P-2FSQH2892E	No direct replacement	
XQVU37P-3FSQH2892E	No direct replacement	
XQVU37P-L2FSQH2892E	No direct replacement	
XCVU45P-2FSVH2104E	No direct replacement	
XCVU45P-1FSVH2104E	No direct replacement	
XCVU45P-1FSVH2892E	No direct replacement	
XCVU45P-2FSVH2892E	No direct replacement	
XCVU45P-3FSVH2104E	No direct replacement	
XCVU45P-3FSVH2892E	No direct replacement	
XCVU45P-L2FSVH2104E	No direct replacement	
XCVU45P-L2FSVH2892E	No direct replacement	
XCVU47P-2FSVH2892E	No direct replacement	
XCVU47P-3FSVH2892E	No direct replacement	
XCVU47P-L2FSVH2892E	No direct replacement	
XCVU47P-1FSVH2892E	No direct replacement	
XCVU57P-1FSVK2892E	No direct replacement	
XCVU57P-2FSVK2892E	No direct replacement	
XCVU57P-3FSVK2892E	No direct replacement	
XCVU57P-L2FSVK2892E	No direct replacement	



Key Dates and Ordering Information

- Final orders (LTB) for FPGA parts will be accepted until December 2, 2024, subject to material availability.
- An order remaining open on or after November 2, 2024, will be deemed Non-Cancellable, Non-Returnable (NCNR).
- Final deliveries (LTS) for parts must occur on or before June 2, 2025.
- For programs requiring shipment past June 2, 2025, please contact your local AMD Sales Representative.
- Return Material Authorizations (RMA) for replacement devices due to quality issues will be accepted until December 2, 2025.

Response

No response required.

Customers are requested to place last-time-buy orders in a timely manner prior to the key dates listed above to avoid product availability issues. If you anticipate that your last time buy requirements will exceed the current rate, please provide your local AMD Sales Representative with a forecast of the requirements. All orders will be supported on a first-come, first-served basis with the non-cancellable and non-returnable (NCNR) condition. For additional information or questions, contact your local AMD Sales representative.

Important Notice: AMD Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the Support website (http://www.xilinx.com/support). Register today and personalize your "Documentation and Design Advisory Alerts" area to include Customer Notifications. AMD Support provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to Xilinx Answer Record 18683.

Revision History

The following table shows the revision history for this document.

Date	Version	Revision
12/04/2023	1.0	Initial Release



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