

<b>PCN Number:</b>		20231127004.2		<b>PCN Date:</b>		November 27, 2023									
<b>Title:</b>		Add Cu as Alternative Wire Base Metal for Selected Device(s)													
<b>Customer Contact:</b>		Change Management team		<b>Dept:</b>		Quality Services									
<b>Proposed 1<sup>st</sup> Ship Date:</b>		May 28, 2024		<b>Sample requests accepted until:</b>		Dec 28, 2023*									
*Sample requests received after Dec 28, 2023 will not be supported.															
<b>Change Type:</b>															
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design	<input type="checkbox"/>	Wafer Bump Material										
<input checked="" type="checkbox"/>	Assembly Process	<input type="checkbox"/>	Data Sheet	<input type="checkbox"/>	Wafer Bump Process										
<input checked="" type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change	<input type="checkbox"/>	Wafer Fab Site										
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site	<input type="checkbox"/>	Wafer Fab Materials										
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process	<input type="checkbox"/>	Wafer Fab Process										
<b>PCN Details</b>															
<b>Description of Change:</b>															
Texas Instruments is pleased to announce the qualification of new assembly material set to add Cu as an additional bond wire option for devices listed in "Product affected" section below. Devices will remain in current assembly facility and piece part changes as follows:															
<table border="1"> <thead> <tr> <th>What</th> <th>Current</th> <th>Additional</th> </tr> </thead> <tbody> <tr> <td>Bond wire, Diameter</td> <td>Die to LF: 0.96 mil Au/1mil Cu Die to Die: 0.96 mil Au</td> <td>Die to LF: 0.8 mil Cu Die to Die: 0.8 mil Cu</td> </tr> </tbody> </table>		What	Current	Additional	Bond wire, Diameter	Die to LF: 0.96 mil Au/1mil Cu Die to Die: 0.96 mil Au	Die to LF: 0.8 mil Cu Die to Die: 0.8 mil Cu								
What	Current	Additional													
Bond wire, Diameter	Die to LF: 0.96 mil Au/1mil Cu Die to Die: 0.96 mil Au	Die to LF: 0.8 mil Cu Die to Die: 0.8 mil Cu													
<b>Reason for Change:</b>															
Continuity of supply. 1) To align with world technology trends and use wiring with enhanced mechanical and electrical properties 2) Maximize flexibility within our Assembly/Test production sites. 3) Cu is easier to obtain and stock															
<b>Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):</b>															
None															
<b>Impact on Environmental Ratings</b>															
Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.															
<table border="1"> <thead> <tr> <th>RoHS</th> <th>REACH</th> <th>Green Status</th> <th>IEC 62474</th> </tr> </thead> <tbody> <tr> <td><input checked="" type="checkbox"/> No Change</td> <td><input checked="" type="checkbox"/> No Change</td> <td><input checked="" type="checkbox"/> No Change</td> <td><input checked="" type="checkbox"/> No Change</td> </tr> </tbody> </table>		RoHS	REACH	Green Status	IEC 62474	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change						
RoHS	REACH	Green Status	IEC 62474												
<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change												
<b>Changes to product identification resulting from this PCN:</b>															
None															
<b>Product Affected:</b>															
ISO1042BQDWQ1		UCC21710QDWQ1		UCC21737QDWRQ1		UCC21755QDWRQ1									
ISO1042BQDWRQ1		UCC21710QDWRQ1		UCC21739QDWQ1		UCC21759QDWQ1									
ISO1042QDWQ1		UCC21717QDWRQ1		UCC21739QDWRQ1		UCC21759QDWRQ1									
ISO1042QDWRQ1		UCC21732QDWQ1		UCC21750QDWQ1											
SN21750QDWRQ1		UCC21732QDWRQ1		UCC21750QDWRQ1											

# Qualification Report

## Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines)

Approve Date 19-October-2023

### Product Attributes

Attributes	Qual Device:	Qual Device:	QBS Process Reference:	QBS Process Reference:	QBS Product Reference:
	ISO6763QDWRQ1	UCC21540QDWKRQ1	UCC23513QDWYQ1	ISO7741EQDWQ1	ISO6763QDWRQ1
Automotive Grade Level	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125
Product Function	Interface	Power Management	Power Management	Interface	Interface
Wafer Fab Supplier	RFAB, RFAB	MH8, MH8, MH8	RFAB, RFAB	MH8, MH8	RFAB, RFAB
Assembly Site	MLA	TAI	TAI	TAI	MLA
Package Group	SOIC	SOIC	SOIC	SOIC	SOIC
Package Designator	DW	DWK	DWY	DW	DW
Pin Count	16	14	6	16	16

QBS: Qual By Similarity

Qual Device ISO6763QDWRQ1 is qualified at MSL2 260C

Qual Device UCC21540QDWKRQ1 is qualified at MSL3 260C

### Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: ISO6763QDWRQ1	Qual Device: UCC21540QDWKRQ1	QBS Process Reference: UCC23513QDWYQ1	QBS Process Reference: ISO7741EQDWQ1	QBS Product Reference: ISO6763QDWRQ1
Test Group A - Accelerated Environment Stress Tests												
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	No Fails	No Fails	-	-	No Fails
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	3/231/0	3/231/0	-	-	1/77/0
AC/UHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Autoclave	121C/15psig	96 Hours	3/231/0	3/231/0	-	-	1/77/0
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	3/231/0	3/231/0	-	-	1/77/0
TC-BP	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull	-	-	1/5/0	1/5/0	-	-	1/5/0
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	150C	1000 Hours	3/135/0	3/135/0	-	-	-
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	175C	500 Hours	-	-	-	-	1/45/0
Test Group B - Accelerated Lifetime Simulation Tests												
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test	125C	1000 Hours	-	-	3/231/0	3/231/0	-
ELFR	B2	AEC Q100-008	3	800	Early Life Failure Rate	125C	48 Hours	-	-	3/2400/0	3/2400/0	-
Test Group C - Package Assembly Integrity Tests												
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/90/0	3/90/0	3/90/0	-
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/90/0	3/90/0	3/90/0	-
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	-	1/15/0	-	1/15/0

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: ISO6763QDWRQ1	Qual Device: UCC21540QDWRQ1	QBS Process Reference: UCC23513QDWRQ1	QBS Process Reference: ISO7741FQDWRQ1	QBS Product Reference: ISO6763QDWRQ1
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-	1/15/0	-	1/15/0
PD	C4	JEDEC JESD22-B100 and B108	3	10	Physical Dimensions	Cpk>1.67	-	-	-	3/30/0	-	-
Test Group D - Die Fabrication Reliability Tests												
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Tddb	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
Test Group E - Electrical Verification Tests												
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	2000 Volts	Device specific data [1]	Device specific data [1]	-	-	1/3/0
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	1500 Volts	Device specific data [1]	Device specific data [1]	-	-	1/3/0
LU	E4	AEC Q100-004	1	6	Latch-Up	Per AEC Q100-004	-	Device specific data [1]	Device specific data [1]	-	-	1/6/0
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	3/90/0	3/90/0	3/90/0	3/90/0	-

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

#### Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40C to +150C

Grade 1 (or Q): -40C to +125C

Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

#### E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

# Qualification Report

## Automotive New Product Qualification Summary

(As per AEC-Q100, AEC-Q006, and JEDEC Guidelines)

Approve Date 28-April-2023

### Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: AMC1305M25QDWQ1	Qual Device: AMC1305M25QDWQ1
Test Group A - Accelerated Environment Stress Tests									
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL3 260C	1 Step	No Fails	No Fails
PC	A1.1	-	3	22	SAM Precon Pre	Review for delamination	1 Step	3/66/0	3/66/0
PC	A1.2	-	3	22	SAM Precon Post	Review for delamination	1 Step	3/66/0	3/66/0
HAST	A2.1	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	-
HAST	A2.1.2	-	3	1	Cross Section, post bHAST, 1X	Post stress cross section	Completed	-	-
HAST	A2.1.3	-	3	30	Wire Bond Shear, post bHAST, 1X	Post stress	Wires	-	-
HAST	A2.1.4	-	3	30	Bond Pull over Stitch, post bHAST, 1X	Post stress	Wires	-	-
HAST	A2.1.5	-	3	30	Bond Pull over Ball, post bHAST, 1X	Post stress	Wires	-	-
HAST	A2.2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	192 Hours	3/231/0	3/231/0
HAST	A2.2.1	-	3	22	SAM Analysis, post bHAST 2X	Review for delamination	Completed	3/66/0	3/66/0
HAST	A2.2.2	-	3	1	Cross Section, post bHAST, 2X	Post stress cross section	Completed	3/3/0	3/3/0
HAST	A2.2.3	-	3	30	Wire Bond Shear, post bHAST, 2X	Post stress	Wires	3/9/0	3/9/0
HAST	A2.2.4	-	3	30	Bond Pull over Stitch, post bHAST, 2X	Post stress	Wires	3/9/0	3/9/0
HAST	A2.2.5	-	3	30	Bond Pull over Ball, post bHAST, 2X	Post stress	Wires	3/9/0	3/9/0
TC	A4.1	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	3/231/0	3/231/0
TC	A4.1.1	-	3	22	SAM Analysis, post TC 1X	Review for delamination	Completed	-	-
TC	A4.1.2	-	3	1	Cross Section, post TC, 1X	Post stress cross section	Completed	-	-
TC	A4.1.3	-	3	30	Wire Bond Shear, post TC, 1X	Post stress	Wires	-	-
TC	A4.1.4	-	3	30	Bond Pull over Stitch, post TC, 1X	Post stress	Wires	-	-
TC	A4.1.5	-	3	30	Bond Pull over Ball, post TC, 1X	Post stress	Wires	-	-
TC	A4.2	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	1000 Cycles	3/210/0	3/210/0
TC	A4.2.1	-	3	22	SAM Analysis, post TC, 2X	Review for delamination	Completed	3/66/0	3/66/0

TC	A4.2.2	-	3	1	Cross Section, post TC, 2X	Post stress cross section	Completed	3/3/0	3/3/0
TC	A4.2.3	-	3	30	Wire Bond Shear, post TC, 2X	Post stress	Wires	3/9/0	3/9/0
TC	A4.2.4	-	3	30	Bond Pull over Stitch, post TC, 2X	Post stress	Wires	3/9/0	3/9/0
TC	A4.2.5	-	3	30	Bond Pull over Ball, post TC, 2X	Post stress	Wires	3/9/0	3/9/0
HTSL	A6.1	JEDEC JESD22-A103	3	45	High Temperature Storage Life	150C	1000 Hours	3/135/0	3/135/0
HTSL	A6.1.1	-	3	1	Cross Section, post HTSL, 1X	Post stress cross section	Completed	-	-
HTSL	A6.2	JEDEC JESD22-A103	3	45	High Temperature Storage Life	150C	2000 Hours	3/132/0	3/132/0
HTSL	A6.2.1	-	3	1	Cross Section, post HTSL, 2X	Post stress cross section	Completed	3/3/0	3/3/0
<b>Test Group B - Accelerated Lifetime Simulation Tests</b>									
<b>Test Group C - Package Assembly Integrity Tests</b>									
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/90/0
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	-
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-
PD	C4	JEDEC JESD22-B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	-	-
<b>Test Group D - Die Fabrication Reliability Tests</b>									
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDDb	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements

QBS: Qual By Similarity

Qual Device AMC1305M25QDWQ1 is qualified at MSL3 260C

Qual Device AMC1305M25QDWQ1 is qualified at MSL3 260C

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

#### Ambient Operating Temperature by Automotive Grade Level:

Grade 0 (or E): -40C to +150C

Grade 1 (or Q): -40C to +125C

Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

#### E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):

Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

ZVEI ID reference: SEM-PA-08

For questions regarding this notice, e-mails can be sent to Change Management team or your local Field Sales Representative.

### **IMPORTANT NOTICE AND DISCLAIMER**

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES “AS IS” AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disdaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI’s products are provided subject to TI’s Terms of Sale ([www.ti.com/legal/termsofsale.html](http://www.ti.com/legal/termsofsale.html)) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI’s provision of these resources does not expand or otherwise alter TI’s applicable warranties or warranty disdaimers for TI products.