

<b>PCN Number:</b>	20231127004.1		<b>PCN Date:</b>	November 27, 2023	
<b>Title:</b>	Add Cu as Alternative Wire Base Metal for Selected Device(s)				
<b>Customer Contact:</b>	Change Management team	<b>Dept:</b>	Quality Services		
<b>Proposed 1<sup>st</sup> Ship Date:</b>	Feb 28, 2024	<b>Sample requests accepted until:</b>	Dec 28, 2023*		
*Sample requests received after Dec 28, 2023 will not be supported.					
<b>Change Type:</b>					
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design	<input type="checkbox"/>	Wafer Bump Material
<input checked="" type="checkbox"/>	Assembly Process	<input type="checkbox"/>	Data Sheet	<input type="checkbox"/>	Wafer Bump Process
<input checked="" type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change	<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site	<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process	<input type="checkbox"/>	Wafer Fab Process
<b>PCN Details</b>					
<b>Description of Change:</b>					
Texas Instruments is pleased to announce the qualification of new assembly material set to add Cu as an additional bond wire option for devices listed in "Product affected" section below. Devices will remain in current assembly facility and piece part changes as follows:					
	<b>What</b>	<b>Current</b>	<b>Additional</b>		
	Bond wire, Diameter	Die to LF: 0.96mil Au/ 1mil Cu Die to Die: 0.96 mil Au	Die to LF: 0.8 mil Cu Die to Die: 0.8 mil Cu		
<b>Reason for Change:</b>					
Continuity of supply. 1) To align with world technology trends and use wiring with enhanced mechanical and electrical properties 2) Maximize flexibility within our Assembly/Test production sites. 3) Cu is easier to obtain and stock					
<b>Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):</b>					
None					
<b>Impact on Environmental Ratings</b>					
Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.					
<b>RoHS</b>	<b>REACH</b>	<b>Green Status</b>	<b>IEC 62474</b>		
<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change	<input checked="" type="checkbox"/> No Change		
<b>Changes to product identification resulting from this PCN:</b>					
None					
<b>Product Affected:</b>					
ISO1042BDW	ISO1042DWR	UCC21732DW	UCC21750DWR		
ISO1042BDWR	UCC21710DW	UCC21732DWR			
ISO1042DW	UCC21710DWR	UCC21750DW			

# Qualification Report

## Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines)

Approve Date 19-October-2023

### Product Attributes

Attributes	Qual Device:	Qual Device:	QBS Process Reference:	QBS Process Reference:	QBS Product Reference:
	ISO6763QDWRQ1	UCC21540QDWKRQ1	UCC23513QDWYQ1	ISO7741FQDWQ1	ISO6763QDWRQ1
Automotive Grade Level	Grade 1	Grade 1	Grade 1	Grade 1	Grade 1
Operating Temp Range (C)	-40 to 125	-40 to 125	-40 to 125	-40 to 125	-40 to 125
Product Function	Interface	Power Management	Power Management	Interface	Interface
Wafer Fab Supplier	RFAB, RFAB	MH8, MH8, MH8	RFAB, RFAB	MH8, MH8	RFAB, RFAB
Assembly Site	MLA	TAI	TAI	TAI	MLA
Package Group	SOIC	SOIC	SOIC	SOIC	SOIC
Package Designator	DW	DWK	DWY	DW	DW
Pin Count	16	14	6	16	16

QBS: Qual By Similarity

Qual Device ISO6763QDWRQ1 is qualified at MSL2 260C

Qual Device UCC21540QDWKRQ1 is qualified at MSL3 260C

### Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: ISO6763QDWRQ1	Qual Device: UCC21540QDWKRQ1	QBS Process Reference: UCC23513QDWYQ1	QBS Process Reference: ISO7741FQDWQ1	QBS Product Reference: ISO6763QDWRQ1
<b>Test Group A - Accelerated Environment Stress Tests</b>												
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	-	No Fails	No Fails	-	-	No Fails
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	3/231/0	3/231/0	-	-	1/77/0
AC/UHAST	A3	JEDEC JESD22-A102/JEDEC JESD22-A118	3	77	Autoclave	121C/15psig	96 Hours	3/231/0	3/231/0	-	-	1/77/0
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	3/231/0	3/231/0	-	-	1/77/0
TC-BP	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull	-	-	1/5/0	1/5/0	-	-	1/5/0
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	150C	1000 Hours	3/135/0	3/135/0	-	-	-
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	175C	500 Hours	-	-	-	-	1/45/0
<b>Test Group B - Accelerated Lifetime Simulation Tests</b>												
HTOL	B1	JEDEC JESD22-A108	3	77	Life Test	125C	1000 Hours	-	-	3/231/0	3/231/0	-
ELFR	B2	AEC Q100-008	3	800	Early Life Failure Rate	125C	48 Hours	-	-	3/2400/0	3/2400/0	-
<b>Test Group C - Package Assembly Integrity Tests</b>												
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/90/0	3/90/0	3/90/0	-
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	3/90/0	3/90/0	3/90/0	3/90/0	-
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	-	-	1/15/0	-	1/15/0

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: ISO6763QDWRQ1	Qual Device: UCC21540QDWRQ1	QBS Process Reference: UCC23513QDWWYQ1	QBS Process Reference: ISO7741FQDWWYQ1	QBS Product Reference: ISO6763QDWRQ1
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	-	-	1/15/0	-	1/15/0
PD	C4	JEDEC JESD22-B100 and B108	3	10	Physical Dimensions	Cpk>1.67	-	-	-	3/30/0	-	-
<b>Test Group D - Die Fabrication Reliability Tests</b>												
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements				
TDD	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements				
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements				
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	-	Completed Per Process Technology Requirements				
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements				
<b>Test Group E - Electrical Verification Tests</b>												
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	2000 Volts	Device specific data [1]	Device specific data [1]	-	-	1/3/0
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	1500 Volts	Device specific data [1]	Device specific data [1]	-	-	1/3/0
LU	E4	AEC Q100-004	1	6	Latch-Up	Per AEC Q100-004	-	Device specific data [1]	Device specific data [1]	-	-	1/6/0
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	3/90/0	3/90/0	3/90/0	3/90/0	-

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

**Ambient Operating Temperature by Automotive Grade Level:**

Grade 0 (or E): -40C to +150C

Grade 1 (or Q): -40C to +125C

Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

**E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):**

Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

For questions regarding this notice, e-mails can be sent to Change Management team or your local Field Sales Representative.

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