

Revision 1.0.0 ADV Issue Date: 11/13/2023

# **CUSTOMER ADVISORY**

# **ADV2321**

Notification to Generate Intel® FPGA JESD204C-FTILE IP license in Quartus v23.2 and later.

## Description:

Intel is notifying customers who are holding an active JESD204C license and are using Quartus v23.1 or earlier that they may need to generate a separate JESD204C-FTILE license file when upgrading to a newer version of Quartus. Customers with active JESD204C licenses can login to the <u>Self Service Licensing Center (SSLC)</u> to generate the new license file for Quartus 23.2 and later, if using / planning to use JESD204C-FTILE IP.

Table 1. List of JESD204C OPNs and License File Status

Primary OPN	Renewal OPN	Quartus 23.1 & earlier	Quartus 23.2 & later
IP-JESD204C	IPR-JESD204C		Included in license file
IP-JESD204C	IPR-JESD204C-	Included in license file	Congrete licence file
FTILE	FTILE		Separate license file

### **Recommended Actions:**

Intel recommends that customers with active licenses for the following OPNs to generate a separate JESD204C-FTILE license file when upgrading to Quartus v23.2 and later. If customers experience challenges generating the IP in SSLC, please reach out to the local Intel representative.

IP-JESD204C

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Intel furthermore recommends that customers upgrade to IPS-JESD204 upon the maintenance expiration date of their JESD204C license: Please refer to <u>ADV2317</u> for more information.

For questions or support, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via: https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html

### **Products Affected:**

Primary OPN	Renewal OPN
IP-JESD204C	IPR-JESD204C
IP-JESD204C-FTILE	IPR-JESD204C-FTILE

# Change Implementation:

#### Table 2

Milestone	Availability
Date change takes effect	Now

#### Contact:

For more information, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via: https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html

# **Customer Notifications Subscription:**

If you would like to receive customer notifications by email, please follow the instructions in ADV 2209

Intel references J-STD-046 guidelines for PCN.

In accordance with J-STD-046, this change is deemed acceptable to the customer if no acknowledgement is received within 30 days from date of notification.

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# **Revision History**

Date	Rev	Description
11/13/2023	1.0.0	Initial Release

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