PCN Numl	ber:	2023	31031007.1			PCN Date:		te:	October 31, 2023	
I ITIA '				AB using qualified Process Technology, Die Revision, and additional						
	Assembly site	(MLA	4)/B	OM options for sele	ect devi	ces				
Customer	Contact:		Cha	ange Management	team	De	pt:		Quality Services	
Proposed 1 st Ship Date:						ple requests cepted until:			Dec 1, 2023*	
*Sample r	equests rece	ived	a fte	r December 1, 202	23 will ı	not b	oe su	ppoı	rted.	
Change Type:										
⊠ Assemb	ly Site		☑ Design					Wafer Bump Material		
	ly Process			Data Sheet				Wat	fer Bump Process	
⊠ Assemb	ly Materials		Part number change				\boxtimes	Wat	fer Fab Site	
Mechan	ical Specification	on	☐ Test Site				\boxtimes	Wafer Fab Materials		
☐ Packing/Shipping/Labeling			☐ Test Process				\boxtimes	Wafer Fab Process		
				PCN Det	ails					

Description of Change:

Texas Instruments is pleased to announce the addition of RFAB using the LBC8LVISO.2 qualified process technology and additional Assembly site (MLA) and BOM options for select devices listed below in the product affected section.

Cı	urrent Fab Site		Additional Fab site			
Current Fab Site	Process		Additional Fab site	Process	Wafer Diameter	
DP1DM5	LBC8LVISO.1	200mm	RFAB	LBC8LVISO.2	300mm	
MIHO8	LBC6LV15U.1	200111111	KFAD	LBC6LV15U.2	300111111	

The die was also changed as a result of the process change.

Construction differences are as follows:

	TAI	MLA
Bond Wire Composition, diameter	Au, 0.96	Cu, 0.8

Qual details are provided in the Qual Data Section.

Reason for Change:

Supply Continuity

Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

Impact on Environmental Ratings:

Checked boxes indicate the status of environmental ratings following implementation of this change. If below boxes are checked, there are no changes to the associated environmental ratings.

RoHS	REACH	Green Status	IEC 62474
☑ No Change	☑ No Change	⊠ No Change	⊠ No Change

Changes to product identification resulting from this PCN:

Fab Site Information:											
Chip Site	Chip Site Origin Code (20L)	Chip Site Country Code (21L)	Chip Site City								
DP1DM5	DM5	USA	Dallas								
MIHO8	MH8	JPN	Ibaraki								
RFAB	RFB	USA	Richardson								

Die Rev:

Current	New
Die Rev [2P]	Die Rev [2P]
А	A

Assembly Site Information:

Assembly Site	Assembly Site Origin (22L)	Assembly Country Code (23L)	Assembly City
TI Taiwan	TAI	TWN	Chung Ho, New Taipei City
MLA	MLA	MYS	Kuala Lumpur

Sample product shipping label (not actual product label)



MSL 2 /260C/1 YEAR SEAL DT MSL 1 /235C/UNLIM 03/29/04 OPT:

LBL: 5A (L)T0:1750



(1P) \$N74L\$07N\$R (Q) 2000 (D) 0336 (31T)LOT: 3959047MLA (4W) TKY(1T) 7523483812

(P) (P) (V) 9933317 (2P) REV: (V) 9933317 (2OL) CSO: SHE (Z1L) CCO:USA (22L) ASO: MLA (23L) ACO: MYS

Product Affected:

ISO7710DR	ISO7720DR	ISO7721DR	ISO7721FDR	
1507710010	1007720DIX	1307721010	10077211010	1

Qualification Report

ISO772X D RFAB REDBULL MLA Approve Date 20-October-2023

Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	#	Test Name	Condition	Duration	Qual Device: ISO7721QDRQ1	Qual Device: ISO7720QDRQ1	QBS Reference: ISO6721BQDRQ1	QBS Reference: UCC23513QDWYQ1	QBS Reference: ISO6763QDWRQ1
HAST	A2	Biased HAST	130C/85%RH	96 Hours	-	-	3/231/0	-	3/231/0
UHAST	A3	Autoclave	121C/15psig	96 Hours	-	-	3/231/0	-	3/231/0
TC	A4	Temperature Cycle	-65C/150C	500 Cycles	1/77/0	-	3/231/0	-	3/231/0
HTSL	A6	High Temperature Storage Life	150C	1000 Hours	-	-	-	-	3/135/0
HTOL	B1	Life Test	125C	1000 Hours	-	-	-	3/231/0	-
ELFR	B2	Early Life Failure Rate	125C	48 Hours	-	-	-	3/2400/0	-
SD	C3	PB Solderability	Precondition w.155C Dry Bake (4 hrs +/- 15 minutes)	-	-	-	1/15/0	-	-
SD	C3	PB-Free Solderability	Precondition w.155C Dry Bake (4 hrs +/- 15 minutes)	-	-	-	1/15/0	-	-
PD	C4	Physical Dimensions	Cpk>1.67	-	1/10/0	-	3/30/0	-	-

Туре	#	Test Name	Condition	Duration	Qual Device: ISO7721QDRQ1	Qual Device: ISO7720QDRQ1	QBS Reference: ISO6721BQDRQ1	QBS Reference: UCC23513QDWYQ1	QBS Reference: ISO6763QDWRQ1
ESD	E2	ESD CDM	-	500 Volts	1/3/0	1/3/0	1/3/0	1/3/0	-
ESD	E2	ESD HBM	-	2000 Volts	1/3/0	1/3/0	1/3/0	1/3/0	-
LU	E4	Latch-Up	Per JESD78	-	1/6/0	1/6/0	1/6/0	1/6/0	-
CHAR	E5	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	1/30/0	1/30/0	3/90/0	3/90/0	3/90/0

- QBS: Qual By Similarity
- Qual Device ISO7721QDRQ1 is qualified at MSL2 260C
- Qual Device ISO7720QDRQ1 is qualified at MSL2 260C
- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- $\bullet \quad \text{The following are equivalent HTSL options based on an activation energy of 0.7eV: } 150\text{C/1k Hours, and } 170\text{C/420 Hours}$
- The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

Quality and Environmental data is available at Tl's external Web site: http://www.ti.com/

TI Qualification ID: R-NPD-2301-069

For questions regarding this notice, e-mails can be sent to the Change Management team or your local Field Sales Representative.

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