10 MBd open collector high speed coupler series production site transfer

For further information, please contact your regional Vishay office.

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Description of Change: The detector chip currently used on the 10 MBd high-speed optocouplers has been discontinued. To ensure supply and to remain cost competitive, the production of these parts will be transferred to one of our sub-contractors where a different chip set has been qualified. There is no impact on the function of the part.

Reason for Change: Transfer to another production site

Expected Influence on Quality/Reliability/Performance: No influence on quality, reliability and performance expected. Nevertheless, we request our customers to test the product in their specific application.

Part Numbers/Series/Families Affected: Please see materials list on the succeeding page.

Vishay Brand(S): Vishay Semiconductors

Time Schedule:

Start Shipment Date: Sun Jan 14, 2024

Sample Availability: Components with the change implemented could start shipping on or after the start shipment date and will be a function of the availability of the material.

Product Identification: By datecode and package outline

Qualification Data: This change has been rigorously qualified by company and industry standard

This PCN is considered approved, without further notification, unless we receive specific customer concerns before Sun Jan 7, 2024 or as specified by contract.

Issued By: Achim Kruck, achim.kruck@vishay.com

6N137	6N137-X006	6N137-X007T	VO0600T	VO0601T
VO0601-X001T	VO0611T	VO0630T	VO0631T	VO0631-X001T
VO0661T	VO2601	VO2601-X006	VO2601-X007T	VO2601-X017T
VO2611	VO2611-X006	VO2611-X007T	VO2611-X016	VO2611-X017T
VO2630	VO2630-X007T	VO2631	VO2631-X006	VO2631-X007T
VO2631-X017T	VO4661	VO4661-X006	VO4661-X007T	VOW137-X001
VOW137-X017T	VOW2611-X001	VOW2611-X017T		

PCN OPT-1300-2023 - 10 MBd open collector high speed coupler series production site transfer Current vs. New Production Site – Key Parameter Comparison

DIP-8 package, single and dual channel parts

Achim Kruck

2023-10-06

1

Electrical Characteristics

2

Input

Current

INPUT						
Input forward voltage	I_F = 10 mA	V_F	1.1	1.4	1.7	V
Reverse current	V_R = 5 V	I_R	-	0.01	10	µA
Input capacitance	f = 1 MHz, V_F = 0 V	C_I	-	55	-	pF

New

INPUT						
Input forward voltage	I_F = 10 mA	V_F	-	1.38	1.70	V
Input forward voltage temperature coefficient	I_F = 10 mA	$\Delta V_F/\Delta T$	-	-1.5	-	mV/K
Input reverse voltage	I_R = 10 µA	BV_R	5	-	-	V
Input threshold current	V_E = 2 V, V_O = 0.6 V, V_{CC} = 5.5 V, I_{OL} (sinking) = 13 mA	I_{TH}	-	2	5	mA
Input capacitance	f = 1 MHz, V_F = 0 V	C_I	-	34	-	pF

3

Output

Current

OUTPUT						
High level supply current (single channel)	V_E = 0.5 V, I_F = 0 mA	I_{CCH}	-	4.1	7	mA
	V_F = V_{CC}, I_F = 0 mA	I_{CCH}	-	3.3	6	mA
High level supply current (dual channel)	I_F = 0 mA	I_{CCH}	-	6.5	12	mA
Low level supply current (single channel)	V_E = 0.5 V, I_F = 10 mA	I_{CCL}	-	4	7	mA
	V_E = V_{CC}, I_F = 10 mA	I_{CCL}	-	3.3	6	mA
Low level supply current (dual channel)	I_F = 10 mA	I_{CCL}	-	6.5	12	mA
High level output current	V_E = 2 V, V_{CC} = 5.5 V, I_F = 250 µA	I_{OH}	-	0.002	1	µA
Low level output voltage	V_F = 2 V, I_F = 5 mA, I_{OL} (sinking) = 13 mA	V_{OL}	-	0.2	0.6	V
Input threshold current	V_F = 2 V, V_{CC} = 5.5 V, I_{OL} (sinking) = 13 mA	I_{TH}	-	2.4	5	mA
High level enable current	V_E = 2 V	I_{EH}	-	-0.8	-1.6	mA
Low level enable current	V_E = 0.5 V	I_{EL}	-	-0.8	-1.6	mA
High level enable voltage		V_{EH}	2	-	-	V
Low level enable voltage		V_{EL}	-	-	0.8	V

New

OUTPUT						
Low level supply current	I_F = 10 mA, V_{CC} = 5.5 V, V_E = 0.5 V	I_{CCL}	-	3.5	5	mA
High level supply current	I_F = 0 mA, V_{CC} = 5.5 V, V_E = 0.5 V	I_{CCH}	-	3.7	5	mA
Low level enable current	V_{CC} = 5.5 V, V_F = 0.5 V	I_{EL}	-	-0.9	-1.6	mA
High level enable current	V_{CC} = 5.5 V, V_F = 2 V	I_{EH}	-	-0.19	-1.6	mA
Low level enable voltage		V_{EL}		-	0.8	V
High level enable voltage		V_{EH}	2	-	-	V
Low level output voltage	V_{CC} = 5.5 V, V_F = 2 V, I_F = 5 mA, I_{OL} (sinking) = 13 mA	V_{OL}	-	0.20	0.60	V
High level output current	V_{CC} = 5.5 V, V_F = 2 V, V_O = 5.5 V, I_F = 250 µA	I_{OH}	-	1	10	µA

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CMTI

Current

COMMON MODE TRANSIENT IMMUNITY (T_{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Common mode transient immunity	$\|V_{CM}\|$ = 10 V, V_{CC} = 5 V, I_F = 0 mA, $V_{O(min.)}$ = 2 V, R_L = 350 Ω, T_{amb} = 25 °C (1)	$\|CM_H\|$	1000	-	-	V/µs
	$\|V_{CM}\|$ = 50 V, V_{CC} = 5 V, I_F = 0 mA, $V_{O(min.)}$ = 2 V, R_L = 350 Ω, T_{amb} = 25 °C (2)	$\|CM_H\|$	5000	10 000	-	V/µs
	$\|V_{CM}\|$ = 1 kV, V_{CC} = 5 V, I_F = 0 mA, $V_{O(min.)}$ = 2 V, R_L = 350 Ω, T_{amb} = 25 °C (3)	$\|CM_H\|$	15 000	25 000	-	V/µs
	$\|V_{CM}\|$ = 10 V, V_{CC} = 5 V, I_F = 7.5 mA, $V_{O(max.)}$ = 0.8 V, R_L = 350 Ω, T_{amb} = 25 °C (1)	$\|CM_L\|$	1000	-	-	V/µs
	$\|V_{CM}\|$ = 50 V, V_{CC} = 5 V, I_F = 7.5 mA, $V_{O(max.)}$ = 0.8 V, R_L = 350 Ω, T_{amb} = 25 °C (2)	$\|CM_L\|$	5000	10 000	-	V/µs
	$\|V_{CM}\|$ = 1 kV, V_{CC} = 5 V, I_F = 7.5 mA, $V_{O(max.)}$ = 0.8 V, R_L = 350 Ω, T_{amb} = 25 °C (3)	$\|CM_L\|$	15 000	25 000	-	V/µs

New

COMMON MODE TRANSIENT IMMUNITY (T_{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART NAME	SYMBOL	MIN.	TYP.	MAX.	UNIT
Logic high common mode transient immunity	V_{CC} = 5 V, $\|V_{CM}\|$ = 1000 V, I_F = 0 mA, V_O > 2.0 V, R_L = 350 Ω	6N137	$\|CM_H\|$	1000	-	-	V/µs
		VO2601		5000	-	-	
		VO2611		15 000	-	-	
Logic low common mode transient immunity	V_{CC} = 5 V, $\|V_{CM}\|$ = 1000 V, I_F = 10 mA, V_O < 0.8 V, R_L = 350 Ω	6N137	$\|CM_L\|$	1000	-	-	
		VO2601		5000	-	-	
		VO2611		15 000	-	-	

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Typical Characteristics

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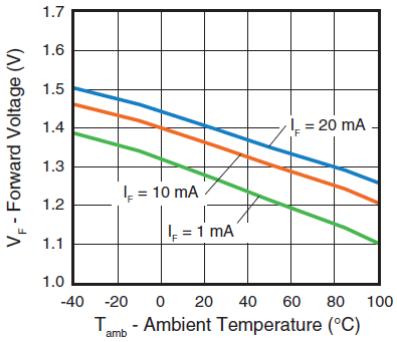
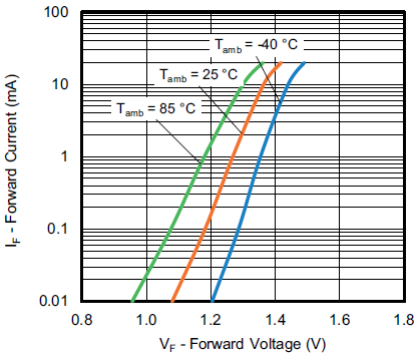
Forward Voltage vs. Ambient Temperature

Current



New



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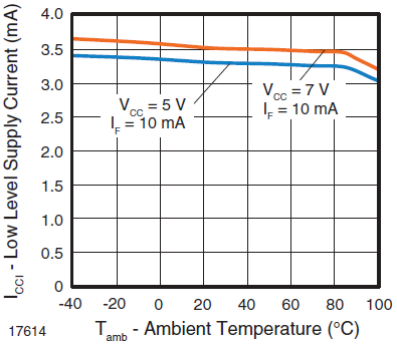
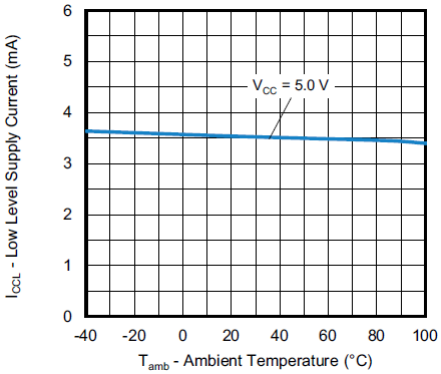
Low Level Supply Current vs. Ambient Temperature

Current



17614

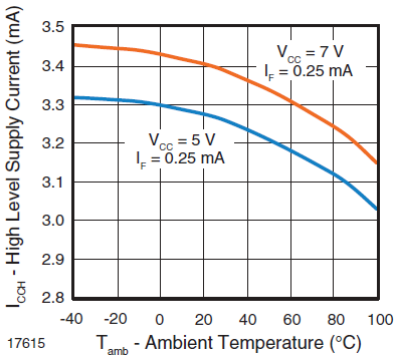
New



8

High Level Supply Current vs. Ambient Temperature

Current



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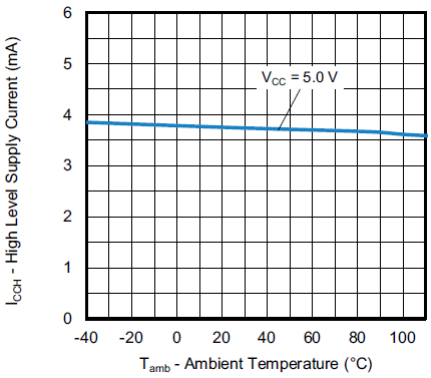

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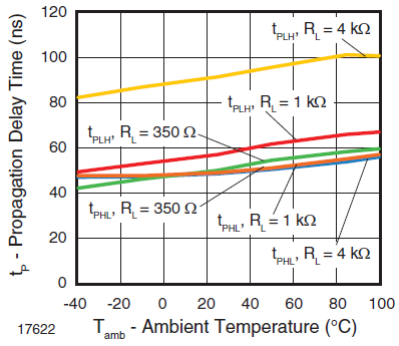
Propagation Delay vs. Ambient Temperature

Current



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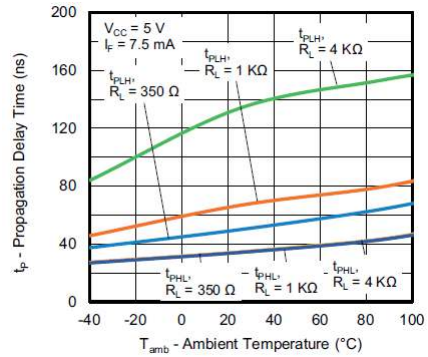
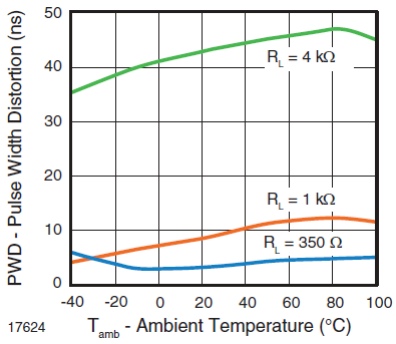


10

Pulse Width Distortion vs. Ambient Temperature

Current



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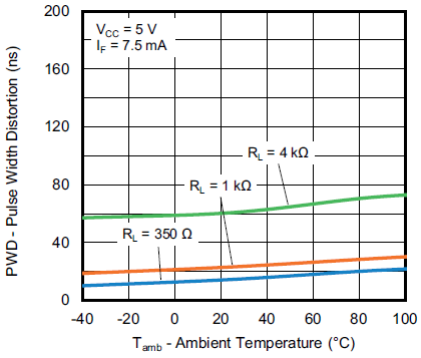

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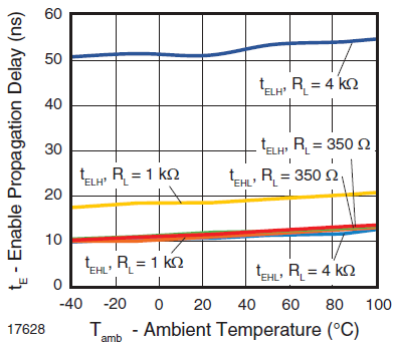
Enable Propagation Delay vs. Ambient Temperature

Current



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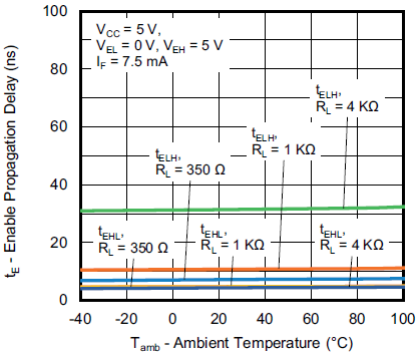

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Package Dimensions & Marking

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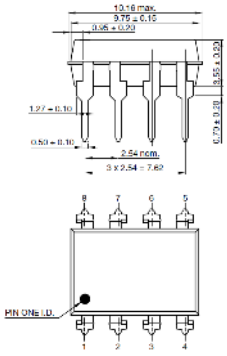
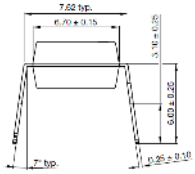
DIP-8

Current





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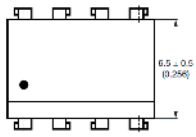

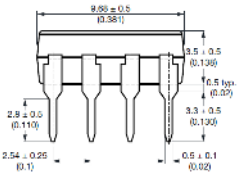

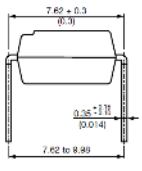

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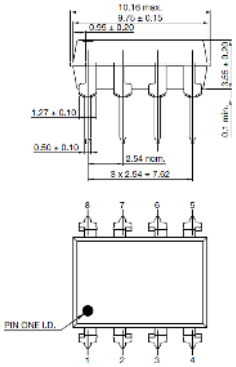
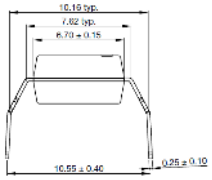
DIP-8, 400 mil (option 6)

Current





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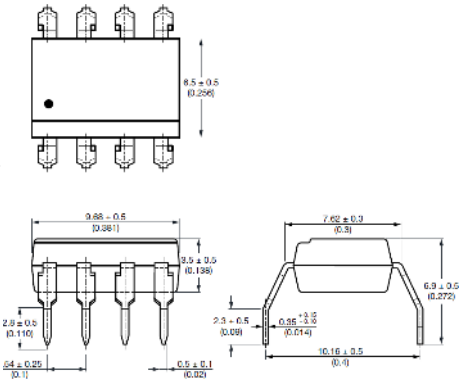

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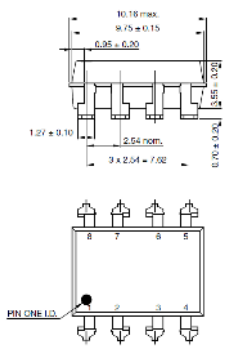
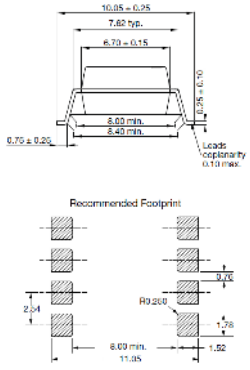
SMD-8

Current





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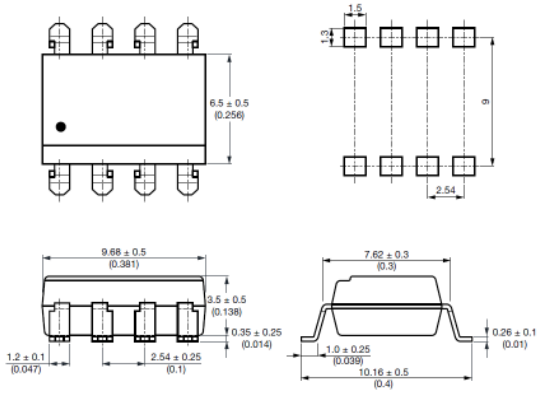

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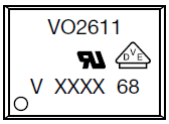
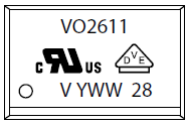
Package Marking

Current



Notes
- XXXX = LMC (lot marking code)
- VDE logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking

New



Notes
- "YWW" is the date code marking (Y = year code, WW = week code)
- VDE logo is only marked on VDE option parts
- Tape and reel suffix (T) is not part of the package marking

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THANK YOU

Contact Info:

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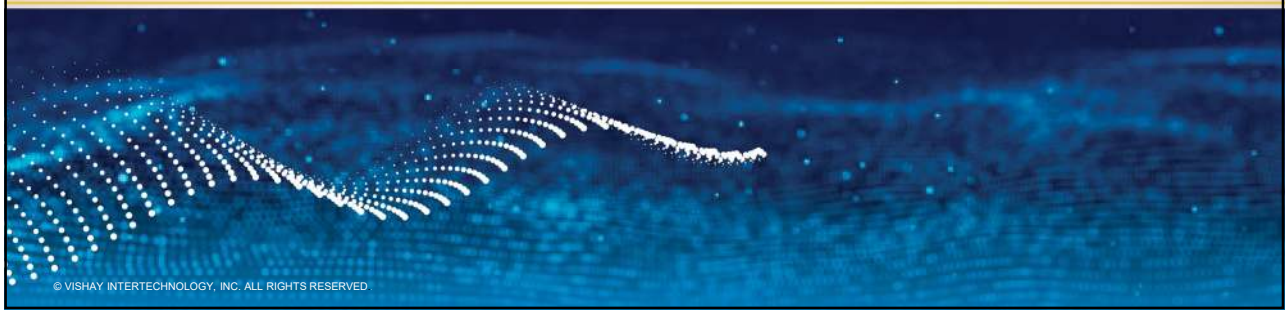
PCN OPT-1300-2023 - 10 MBd open collector high speed coupler series production site transfer Current vs. New Production Site – Key Parameter Comparison

SOIC-8 package, single and dual channel parts

Achim Kruck

2023-10-06



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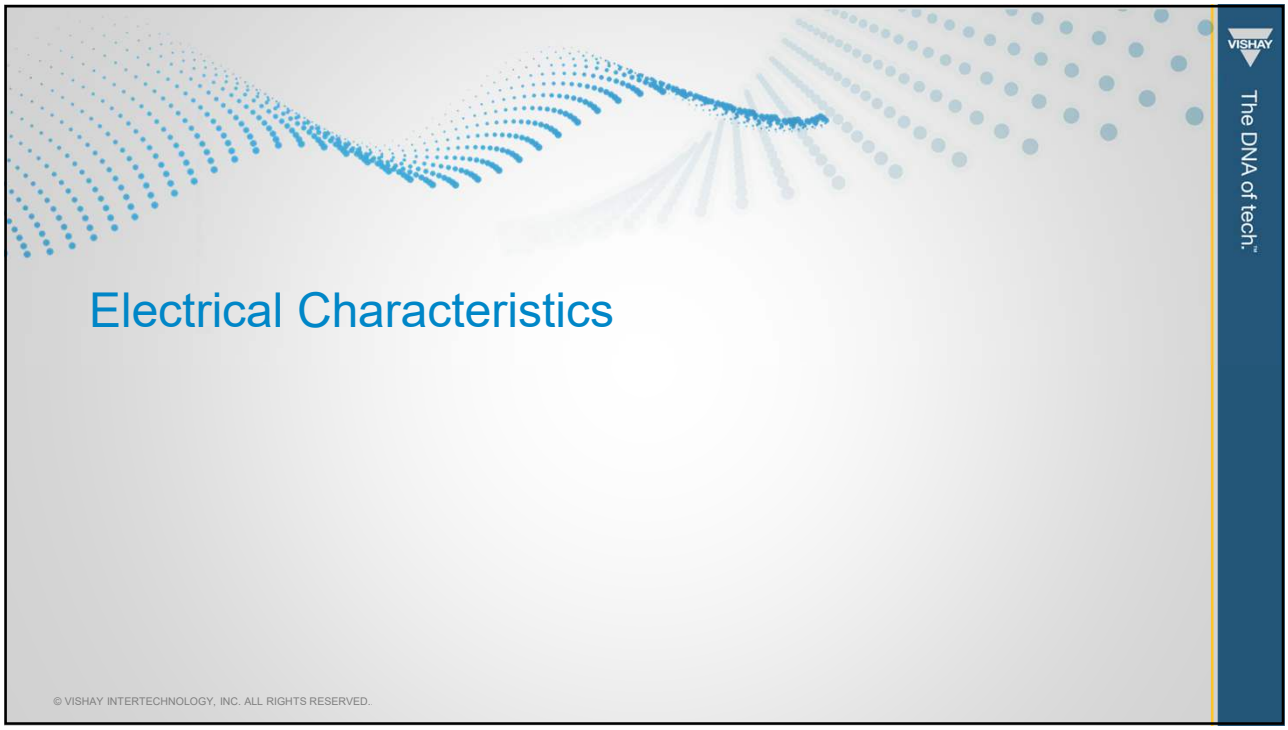

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Input

Current

INPUT						
Input forward voltage	I_F = 10 mA	V_F	1.1	1.4	1.7	V
Reverse current	V_R = 5 V	I_R	-	0.01	10	µA
Input capacitance	f = 1 MHz, V_F = 0 V	C_I	-	55	-	pF

New

INPUT						
Input forward voltage	I_F = 10 mA	V_F	-	1.38	1.70	V
Input forward voltage temperature coefficient	I_F = 10 mA	$\Delta V_F/\Delta T$	-	-1.5	-	mV/K
Input reverse voltage	I_R = 10 µA	BV_R	5	-	-	V
Input threshold current	V_E = 2 V, V_O = 0.6 V, V_{CC} = 5.5 V, I_{OL} (sinking) = 13 mA	I_{TH}	-	2	5	mA
Input capacitance	f = 1 MHz, V_F = 0 V	C_I	-	34	-	pF

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Output

Current

OUTPUT						
High level supply current (single channel)	V_E = 0.5 V, I_F = 0 mA	I_{CCH}	-	4.1	7	mA
	V_E = V_{CC}, I_F = 0 mA	I_{CCH}	-	3.3	6	mA
High level supply current (dual channel)	I_F = 10 mA	I_{CCH}	-	6.5	12	mA
Low level supply current (single channel)	V_E = 0.5 V, I_F = 10 mA	I_{CCL}	-	4	7	mA
	V_E = V_{CC}, I_F = 10 mA	I_{CCL}	-	3.3	6	mA
Low level supply current (dual channel)	I_F = 10 mA	I_{CCL}	-	6.5	12	mA
High level output current	V_F = 2 V, V_O = 5.5 V, I_F = 250 µA	I_{OH}	-	0.002	1	µA
Low level output voltage	V_E = 2 V, I_F = 5 mA, I_{OL} (sinking) = 13 mA	V_{OL}	-	0.2	0.6	V
Input threshold current	V_E = 2 V, V_O = 5.5 V, I_{OL} (sinking) = 13 mA	I_{TH}	-	2.4	5	mA
High level enable current		I_{EH}	-	- 0.6	- 1.6	mA
Low level enable current		I_{EL}	-	- 0.8	- 1.6	mA
High level enable voltage		V_{EH}	2	-	-	V
Low level enable voltage		V_{EL}	-	-	0.8	V

New

OUTPUT						
Low level supply current	I_F = 10 mA, V_{CC} = 5.5 V, V_E = 0.5 V	I_{CCL}	-	3.5	5	mA
High level supply current	I_F = 0 mA, V_{CC} = 5.5 V, V_E = 0.5 V	I_{CCH}	-	3.7	5	mA
Low level enable current	V_{CC} = 5.5 V, V_E = 0.5 V	I_{EL}	-	-0.9	-1.6	mA
High level enable current	V_{CC} = 5.5 V, V_E = 2 V	I_{EH}	-	-0.6	-1.6	mA
Low level enable voltage		V_{EL}	-	-	0.8	V
High level enable voltage		V_{EH}	2	-	-	V
Low level output voltage	V_{CC} = 5.5 V, V_E = 2 V, I_F = 5 mA, I_{OL} (sinking) = 13 mA	V_{OL}	-	0.20	0.60	V
High level output current	V_{CC} = 5.5 V, V_E = 2 V, V_O = 5.5 V, I_F = 250 µA	I_{OH}	-	1	10	µA

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CMTI

Current

COMMON MODE TRANSIENT IMMUNITY						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Common mode transient immunity (high)	$\|V_{CM}\|$ = 10 V, V_{CC} = 5 V, I_F = 0 mA, $V_{O(min)}$ = 2 V, R_L = 350 Ω, T_{amb} = 25 °C(1)	$\|CM_H\|$	1000			V/µs
	$\|V_{CM}\|$ = 50 V, V_{CC} = 5 V, I_F = 0 mA, $V_{O(min)}$ = 2 V, R_L = 350 Ω, T_{amb} = 25 °C(2)	$\|CM_H\|$	5000	10 000		V/µs
	$\|V_{CM}\|$ = 1 kV, V_{CC} = 5 V, I_F = 0 mA, $V_{O(min)}$ = 2 V, R_L = 350 Ω, T_{amb} = 25 °C(3)	$\|CM_H\|$	15 000	25 000		V/µs
Common mode transient immunity (low)	$\|V_{CM}\|$ = 10 V, V_{CC} = 5 V, I_F = 7.5 mA, $V_{O(max)}$ = 0.8 V, R_L = 350 Ω, T_{amb} = 25 °C(1)	$\|CM_L\|$	1000			V/µs
	$\|V_{CM}\|$ = 50 V, V_{CC} = 5 V, I_F = 7.5 mA, $V_{O(max)}$ = 0.8 V, R_L = 350 Ω, T_{amb} = 25 °C(2)	$\|CM_L\|$	5000	10 000		V/µs
	$\|V_{CM}\|$ = 1 kV, V_{CC} = 5 V, I_F = 7.5 mA, $V_{O(max)}$ = 0.8 V, R_L = 350 Ω, T_{amb} = 25 °C(3)	$\|CM_L\|$	15 000	25 000		V/µs

New

COMMON MODE TRANSIENT IMMUNITY (T_{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART NUMBER	SYMBOL	MIN.	TYP.	MAX.	UNIT
Logic high common mode transient immunity	V_{CC} = 5 V, $\|V_{CM}\|$ = 1000 V, I_F = 0 mA, V_O > 2.0 V, R_L = 350 Ω		$\|CM_H\|$	VO0600	1 000	-	-	V/µs
			$\|CM_H\|$	VO0601	5 000	-	-	V/µs
			$\|CM_H\|$	VO0611	15 000	-	-	V/µs
Logic low common mode transient immunity	V_{CC} = 5 V, $\|V_{CM}\|$ = 1000 V, I_F = 10 mA, V_O < 0.8 V, R_L = 350 Ω		$\|CM_L\|$	VO0600	1 000	-	-	V/µs
			$\|CM_L\|$	VO0601	5 000	-	-	V/µs
			$\|CM_L\|$	VO0611	15 000	-	-	V/µs

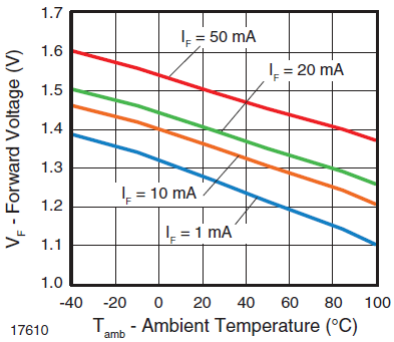
23

Typical Characteristics

24

Forward Voltage vs. Ambient Temperature

Current



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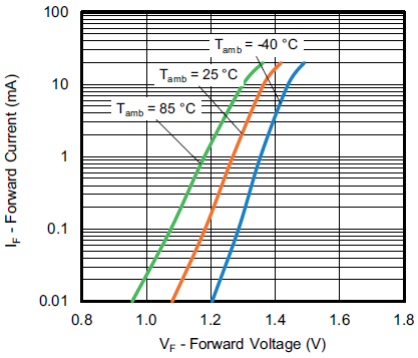
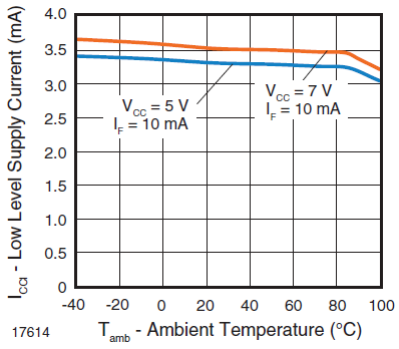


25

Low Level Supply Current vs. Ambient Temperature

Current



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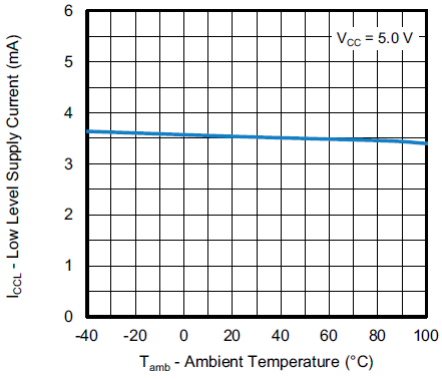
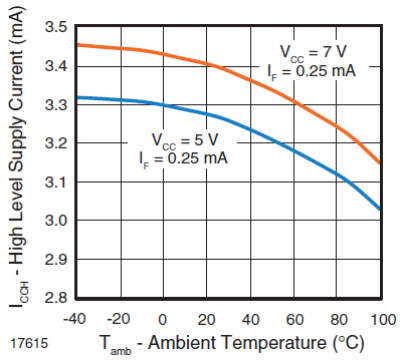


26

High Level Supply Current vs. Ambient Temperature

Current



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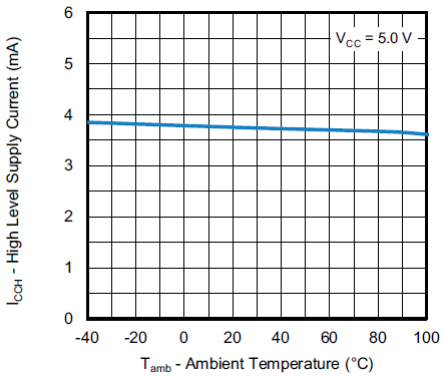
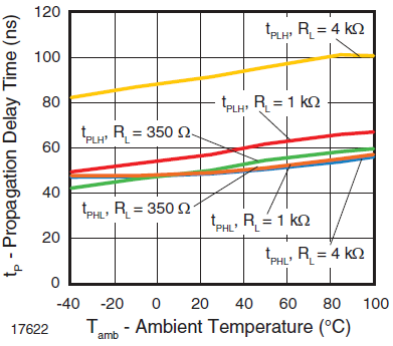


27

Propagation Delay vs. Ambient Temperature

Current



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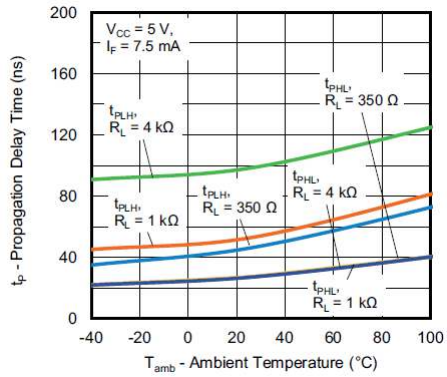
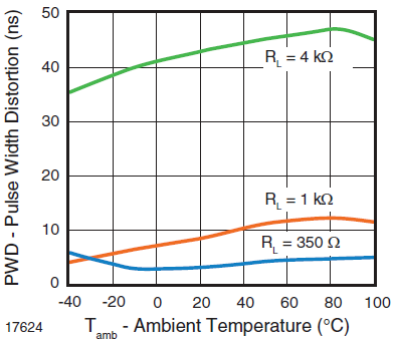


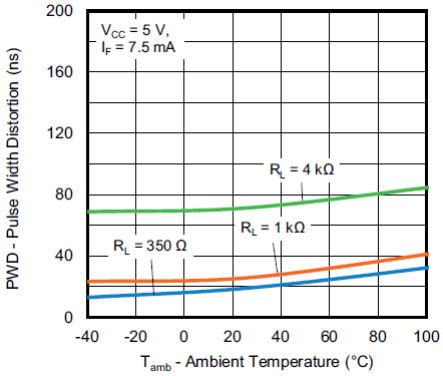
28

Pulse Width Distortion vs. Ambient Temperature

Current



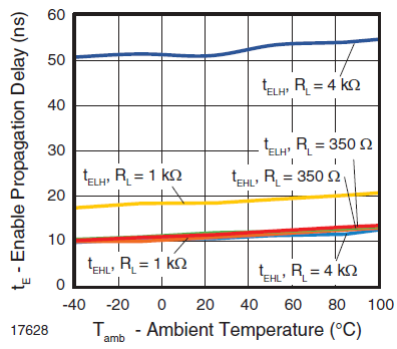
New



29

Enable Propagation Delay vs. Ambient Temperature

Current



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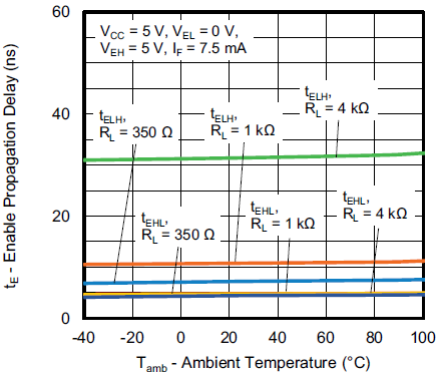

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Package Dimensions & Marking

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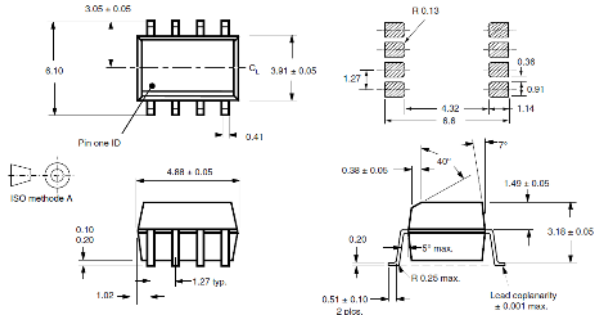
SOIC-8

Current



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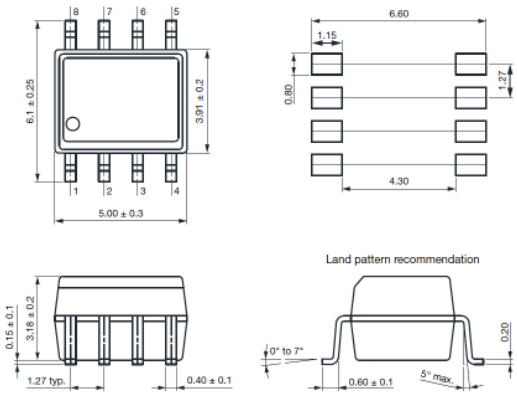
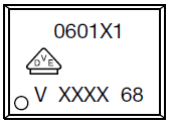


32

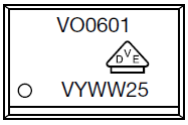
Package Marking

Current



Notes
- XXXX = LMC (lot marking code)
- VDE logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking

New



Notes
- "YWW" is the date code marking (Y = year code, WW = week code)
- VDE logo is only marked on VDE option parts
- Tape and reel suffix (T) is not part of the package marking

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THANK YOU

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e-mail: achim.kruck@vishay.com

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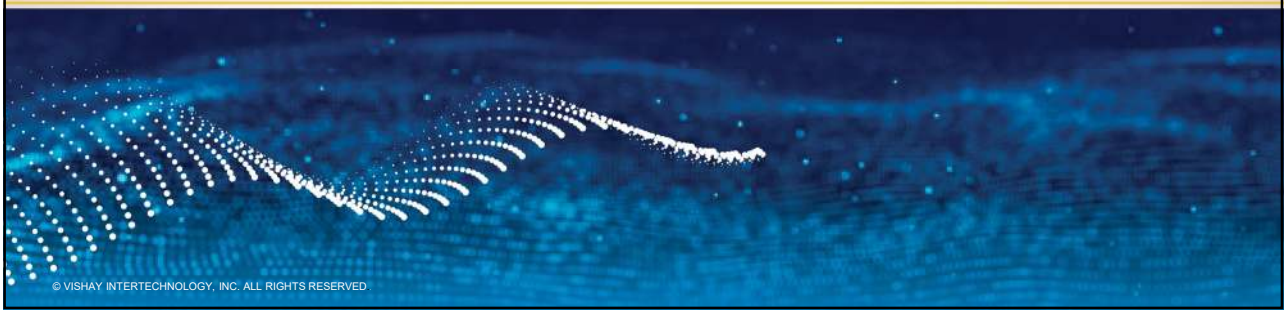
PCN OPT-1300-2023 - 10 MBd open collector high speed coupler series production site transfer Current vs. New Production Site – Key Parameter Comparison

VISHAY
The DNA of tech.™

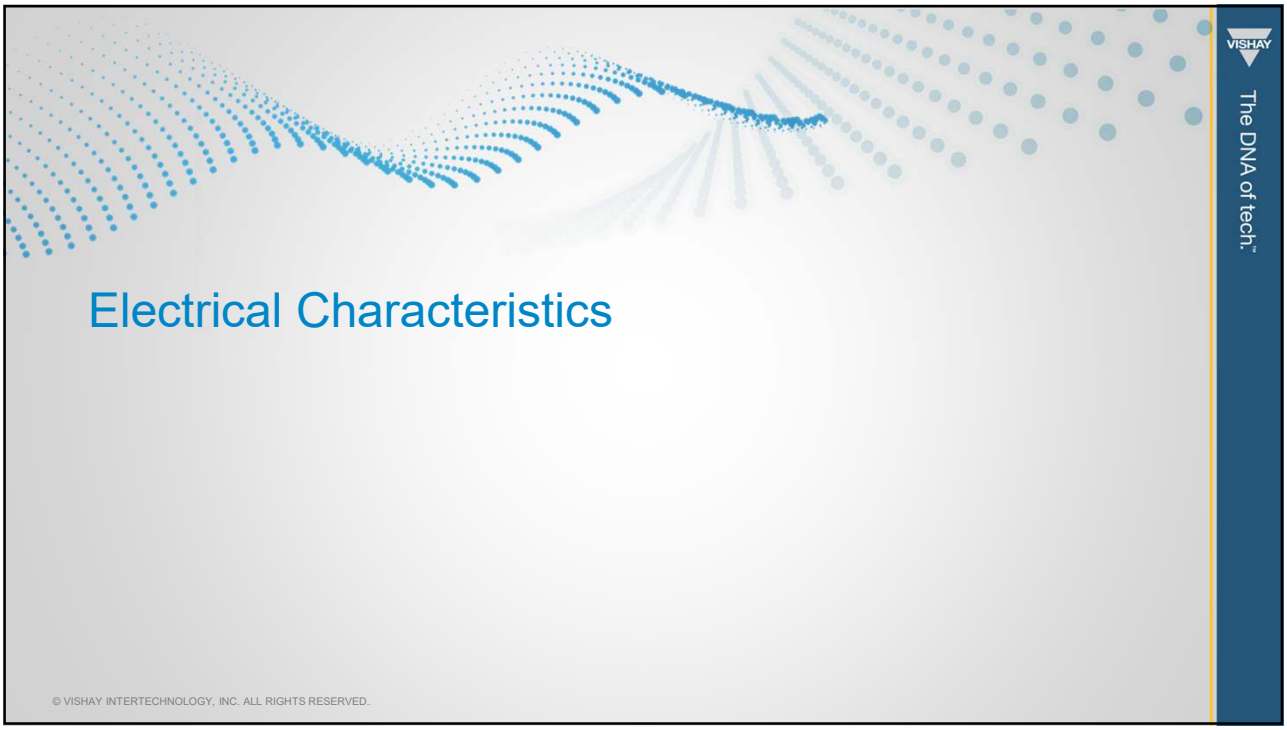
WDIP-8 package, single and dual channel parts

Achim Kruck

2023-10-06



35



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Input

Current

INPUT						
Input forward voltage	I_F = 10 mA	V_F	1.1	1.4	1.7	V
Reverse current	V_R = 5 V	I_R	-	0.01	10	µA
Input capacitance	f = 1 MHz, V_F = 0 V	C_I	-	38	-	pF

New

INPUT						
Input forward voltage	I_F = 10 mA	V_F	-	1.38	1.70	V
Input forward voltage temperature coefficient	I_F = 10 mA	$\Delta V_F/\Delta T$	-	-1.5	-	mV/K
Input reverse voltage	I_R = 10 µA	BV_R	5	-	-	V
Input threshold current	V_E = 2 V, V_O = 0.6 V, V_{CC} = 5.5 V, I_{OL} (sinking) = 13 mA	I_{TH}	-	2	5	mA
Input capacitance	f = 1 MHz, V_F = 0 V	C_I	-	34	-	pF

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Output

Current

OUTPUT						
High level supply current	V_E = 0.5 V, I_F = 0 mA	I_{OCH}	-	4.3	10	mA
	V_E = V_{CC}, I_F = 0 mA	I_{OCH}	-	3.3	-	mA
Low level supply current	V_E = 0.5 V, I_F = 10 mA	I_{OCL}	-	4.3	13	mA
	V_E = V_{CC}, I_F = 10 mA	I_{OCL}	-	3.3	6	mA
High level output current	V_E = 2 V, V_O = 5.5 V, I_F = 250 µA	I_{OH}	-	0.02	10	µA
Low level output voltage	V_E = 2 V, I_F = 5 mA, I_{OL} (sinking) = 13 mA	V_{OL}	-	0.2	0.6	V
Input threshold current	V_E = 2 V, V_O = 0.6 V, I_{OL} (sinking) = 13 mA	I_{TH}	-	2.4	5	mA
Input-output capacitance	f = 1 MHz, T_{amb} = 25 °C	C_{IO}	-	0.9	-	pF
High level enable current	V_E = 2 V	I_{EH}	-	-0.6	-1.6	mA
Low level enable current	V_E = 0.5 V	I_{EL}	-	-0.8	-1.6	mA
High level enable voltage		V_{EH}	2	-	-	V
Low level enable voltage		V_{EL}	-	-	0.8	V

New

OUTPUT						
Low level supply current	I_F = 10 mA, V_{CC} = 5.5 V, V_E = 0.5 V	I_{CCL}	-	3.5	5	mA
High level supply current	I_F = 0 mA, V_{CC} = 5.5 V, V_E = 0.5 V	I_{CCH}	-	3.7	5	mA
Low level enable current	V_{CC} = 5.5 V, V_E = 0.5 V	I_{EL}	-	-0.9	-1.6	mA
High level enable current	V_{CC} = 5.5 V, V_E = 2 V	I_{EH}	-	-0.6	-1.6	mA
Low level enable voltage		V_{EL}	-	-	0.8	V
High level enable voltage		V_{EH}	2	-	-	V
Low level output voltage	V_{CC} = 5.5 V, V_E = 2 V, I_F = 5 mA, I_{OL} (sinking) = 13 mA	V_{OL}	-	0.20	0.60	V
High level output current	V_{CC} = 5.5 V, V_E = 2 V, V_O = 5.5 V, I_F = 250 µA	I_{OH}	-	1	10	µA

38

CMTI

Current

COMMON MODE TRANSIENT IMMUNITY							
PARAMETER	TEST CONDITION	DEVICE	SYMBOL	MIN.	TYP.	MAX.	UNIT
Common mode transient immunity	$\|V_{CM}\|$ = 1 kV, V_{CC} = 5 V, I_F = 0 mA (1)(2)(3)(4)	VOW137	$\|CM_H\|$	10 000			V/µs
	$\|V_{CM}\|$ = 1 kV, V_{CC} = 5 V, I_F = 0 mA (1)(2)(5)	VOW2611	$\|CM_H\|$	25 000	40 000		V/µs
	$\|V_{CM}\|$ = 1 kV, V_{CC} = 5 V, I_F = 7.5 mA (1)(2)(3)(4)	VOW137	$\|CM_L\|$	10 000			V/µs
	$\|V_{CM}\|$ = 1 kV, V_{CC} = 5 V, I_F = 7.5 mA (1)(2)(5)	VOW2611	$\|CM_L\|$	25 000	40 000		V/µs

New

COMMON MODE TRANSIENT IMMUNITY (T_{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	DEVICE	SYMBOL	MIN.	TYP.	MAX.	UNIT
Common mode transient immunity	$\|V_{CM}\|$ = 1 kV, VV_{CC} = 5 V, I_F = 0 mA	VOW137	$\|CM_H\|$	10 000	-	-	V/µs
	$\|V_{CM}\|$ = 1 kV, VV_{CC} = 5 V, I_F = 0 mA	VOW2611	$\|CM_H\|$	25 000	-	-	V/µs
	$\|V_{CM}\|$ = 1 kV, VV_{CC} = 5 V, I_F = 7.5 mA	VOW137	$\|CM_L\|$	10 000	-	-	V/µs
	$\|V_{CM}\|$ = 1 kV, VV_{CC} = 5 V, I_F = 7.5 mA	VOW2611	$\|CM_L\|$	25 000	-	-	V/µs

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Typical Characteristics

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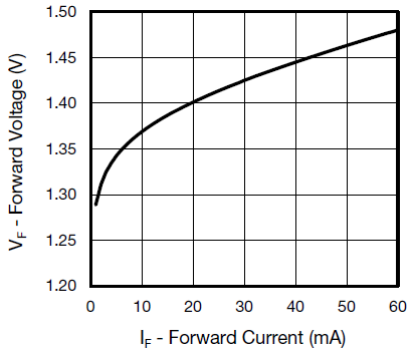
Forward Voltage vs. Ambient Temperature

Current



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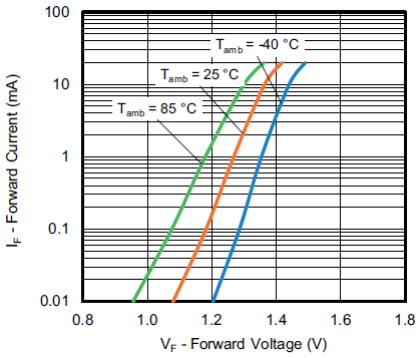

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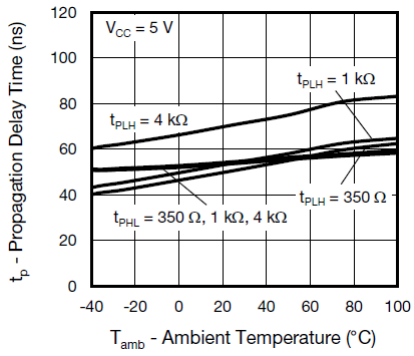
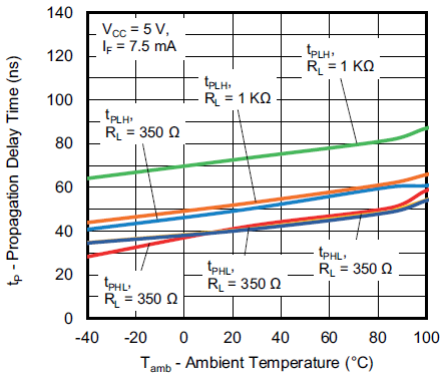
Propagation Delay vs. Ambient Temperature

Current



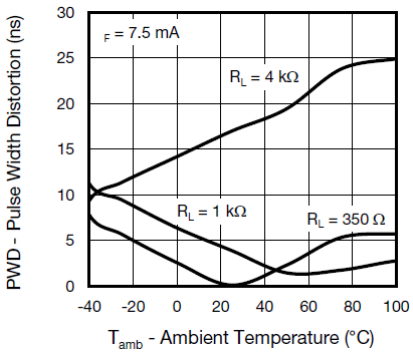
New



42

Pulse Width Distortion vs. Ambient Temperature

Current



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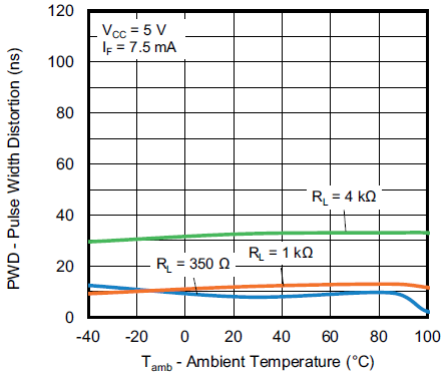
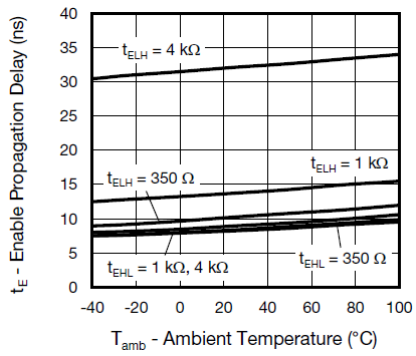


43

Enable Propagation Delay vs. Ambient Temperature

Current



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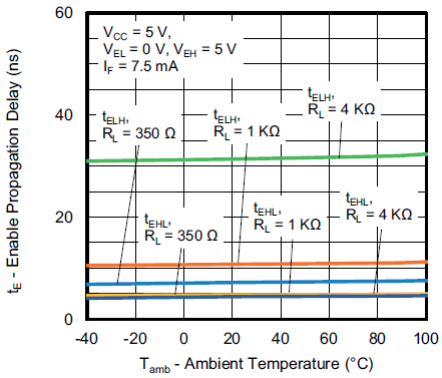

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Package Dimensions & Marking

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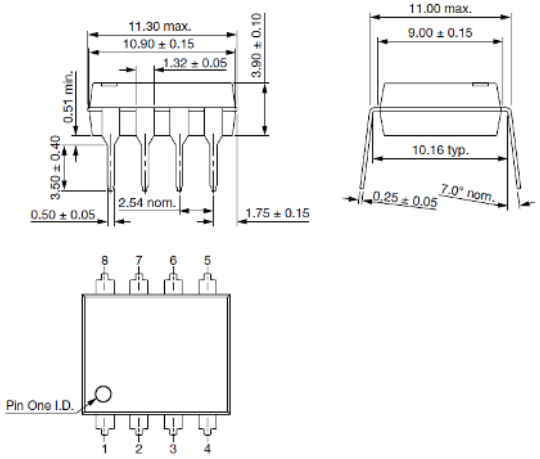
DIP-8, 400 mil, Widebody

Current



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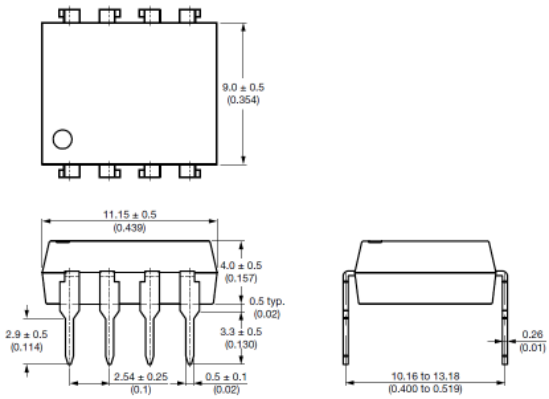
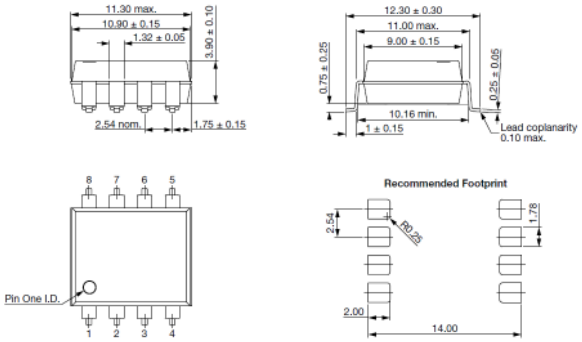


46

SMD-8, 400 mil, Widebody

Current



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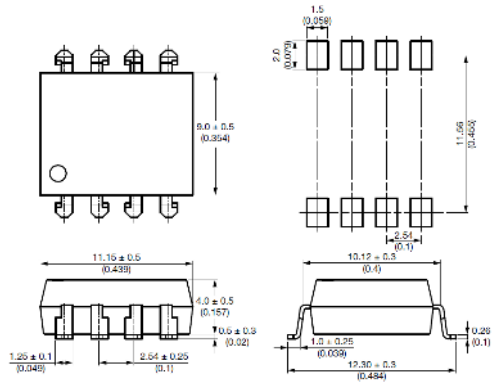

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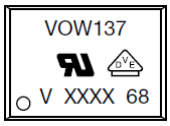
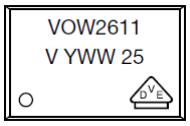
Package Marking

Current



Notes
- XXXX = LMC (lot marking code)
- Tape and reel suffix (T) is not part of the package marking

New



Notes
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- VDE logo is only marked on VDE option parts
- Tape and reel suffix (T) is not part of the package marking

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