



Product Change Notification / SYST-18AULC313

Date:

19-Oct-2023

Product Category:

Clock and Timing - Clock and Data Distribution

PCN Type:

Document Change

Notification Subject:

Data Sheet - SY75608/612 - 8 or 12 Output Ultra-Low Additive Jitter PCIe 1/2/3/4/5 Clock Buffer

Affected CPNs:

[SYST-18AULC313_Affected_CPN_10192023.pdf](#)

[SYST-18AULC313_Affected_CPN_10192023.csv](#)

Notification Text:

SYST-18AULC313

Microchip has released a new Datasheet for the SY75608/612 - 8 or 12 Output Ultra-Low Additive Jitter PCIe 1/2/3/4/5 Clock Buffer of devices. If you are using one of these devices please read the document located at [SY75608/612 - 8 or 12 Output Ultra-Low Additive Jitter PCIe 1/2/3/4/5 Clock Buffer](#).

Notification Status: Final

Description of Change: Updated Silicon Revision Number in Table 6-2 to "0010".

Impacts to Data Sheet: See above details.

Reason for Change: To improve productivity.

Change Implementation Status: Complete

Date Document Changes Effective: 19 Oct 2023

NOTE: Please be advised that this is a change to the document only the product has not been changed.

Markings to Distinguish Revised from Unrevised Devices: N/A

Attachments:

[SY75608/612 - 8 or 12 Output Ultra-Low Additive Jitter PCIe 1/2/3/4/5 Clock Buffer](#)

Please contact your local [Microchip sales office](#) with questions or concerns regarding this notification.

Terms and Conditions:

If you wish to receive Microchip PCNs via email please register for our PCN email service at our [PCN home page](#) select register then fill in the required fields. You will find instructions about registering for Microchips PCN email service in the [PCN FAQ](#) section.

If you wish to change your PCN profile, including opt out, please go to the [PCN home page](#) select login and sign into your myMicrochip account. Select a profile option from the left navigation bar and make the applicable selections.

Affected Catalog Part Numbers (CPN)

SY75608TWL

SY75608TWL-TR

SY75612TWL

SY75612TWL-TR