



# CUSTOMER ADVISORY

## ADV2317

### Replacement of Intel® FPGA single JESD204 IP OPNs with Intel® FPGA JESD204 IP Bundle

---

#### Description:

Intel® is announcing the replacement of all JESD204 IP OPNs (orderable part numbers) with a single JESD204 IP OPN. The single JESD204 IP OPN will have one price for all JESD204 IPs from Intel PSG (Programmable Solutions Group). A customer who now purchases IPS-JESD204 will now have the following IP licenses included in the purchased bundle:

- IP-JESD204C, IP-JESD204C-FTILE, IP-JESD204B, IP-JESD204B-FTILE

Refer to the table below for new orderable part numbers (OPNs):

Table 1

Primary OPN	Renewal OPN	Order Status	Notes
IPS-JESD204	IPSR-JESD204	Orderable	New bundled OPN that includes IP-JESD204C, IP-JESD204C-FTILE, IP-JESD204B, IP-JESD204B-FTILE
IP-JESD204C	IPR-JESD204C	No Longer Orderable	
IP-JESD204C-FTILE	IPR-JESD204C-FTILE		
IP-JESD204B	IPR-JESD204B		
IP-JESD204B-FTILE	IPR-JESD204B-FTILE		

## Recommended Actions:

Intel recommends that customers with active licenses for the for the following OPNs to upgrade to IPS-JESD204 upon expiration date of license:

- IP-JESD204C
- IP-JESD204C-FTILE
- IP-JESD204B
- IP-JESD204B-FTILE

For questions or support, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

## Products Affected:

Primary OPN	Renewal OPN
IP-JESD204C	IPR-JESD204C
IP-JESD204C-FTILE	IPR-JESD204C-FTILE
IP-JESD204B	IPR-JESD204B
IP-JESD204B-FTILE	IPR-JESD204B-FTILE
IPS-JESD204 (Bundle)	IPSR-JESD204 (Bundle)

## Change Implementation:

Table 2

Milestone	Availability
Date change takes effect	Now

## Contact:

For more information, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

## Customer Notifications Subscription

If you would like to receive customer notifications by email, please follow the instructions in [ADV 2209](#)

---

*Intel references J-STD-046 guidelines for PCN.*

*In accordance with J-STD-046, this change is deemed acceptable to the customer if no acknowledgement is received within 30 days from date of notification.*

---

## Revision History

Date	Rev	Description
09/01/2023	1.0.0	Initial Release

©2023 Intel Corporation. All rights reserved. Intel, the Intel logo, Altera, Arria, Cyclone, Enpirion, Max, Nios, Quartus and Stratix words and logos are trademarks of Intel Corporation or its subsidiaries in the U.S. and/or other countries. Other marks and brands may be claimed as the property of others. Intel reserves the right to make changes to any products and services at any time without notice. Intel assumes no responsibility or liability arising out of the application or use of any information, product, or service described herein except as expressly agreed to in writing by Intel. Intel customers are advised to obtain the latest version of device specifications before relying on any published information and before placing orders for products or services.