J-Link BASE

Overview

The BASE version of the market-leading debug probe family. Top models of the J-Link family are J-Link PRO, J-Link ULTRA+, and J-Link PLUS.

J-Link BASE is available in two form factors with **identical function**: J-Link BASE Classic and J-Link BASE Compact.

J-Link BASE is a USB-powered JTAG debug probe supporting a large number of CPU cores. Based on a 32-bit RISC CPU, it can communicate at high speed with the supported target CPUs. J-Link is used around the world in tens of thousands places for development and production (flash programming) purposes.

The J-Link debug probes are supported by all major IDEs including Eclipse, GDB-based IDEs and SEGGER Embedded Studio. For a complete list, please refer to supported IDEs. Including all models, more than 1,000,000 J-Links have been shipped so far, making J-Link probably the most popular debug probe for Arm cores and the de-facto standard.

Specification

- Power supply: USB
- Download speed to RAM: 1.0MB/s
- Max target inteface speed: 15MHz
- Max SPI interface speed: 12MHz
- Max SWO speed: 30MHz
- High speed sampling bandwith: 1kHz
- Supported target voltage: 3.3V

Key features

- Download speed up to 1 MB/s
- Built-in VCOM functionality
- Supports a broad range of microcontrollers
- Free software updates

Package content

- J-Link BASE Classic or Compact version

- USB cable

- 20-Pin Target cable

Order number

J-Link BASE Classic 8.08.00

J-Link BASE Compact 8.19.00