

To : KII



Engineering Change Notice (T-55265GD057J-LW-ACN)

2022/8/2

KYOCERA Corporation
Corporate Display Group
Display Engineering Division
Product Engineering Department

Approved	Checked	Prepared
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Control No. : EDPM2-2207-A0-1100

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Thank you for your continued support.
For the following reasons, we will apply for a change in the materials used in our products that are currently being mass-produced.
We apologize for the inconvenience, and thank you for your understanding.

- 1. Applicable Product T-55265GD057J-LW-ACN
- 2. Background

The supply of materials used in this product continues to be tight especially electronic components on PCB.
We are unable to secure the sufficient number of components that is causing inconvenience to our customers in terms of delivery.
Because this product is old in design and it is unlikely that the situation will be improved in the procurement of electronic components in near future, we would like to propose a renewal to products using newer materials and electronic components in order to stabilize the delivery.
For more details, please refer to the following pages.
We appreciate your understanding and cooperation.

- 3. Schedule

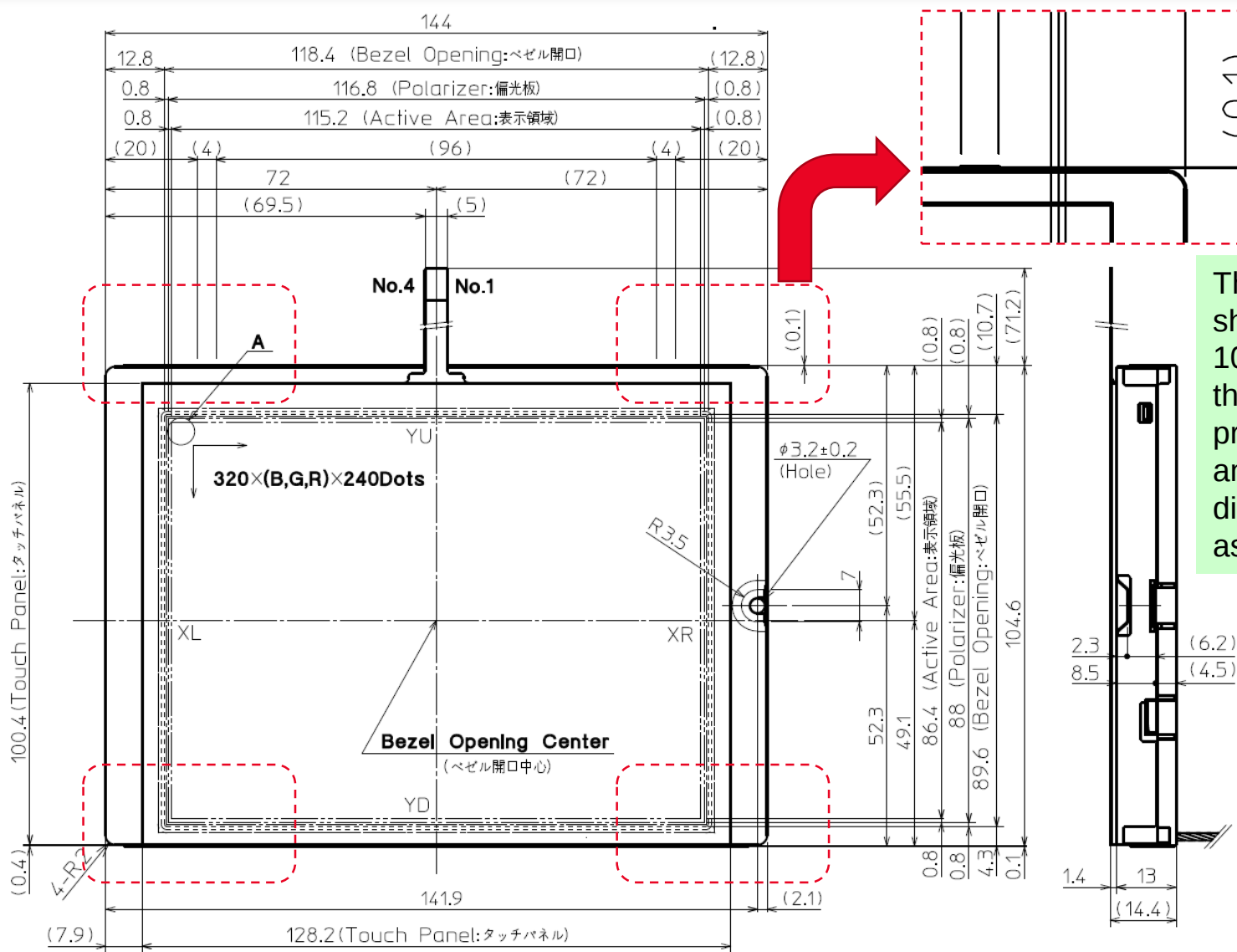
Sample shipment	:	August, 2022
Approval request	:	September, 2022
Planned change in	:	Notify applicable lot separately

For the 5.7inch QVGA [T-55265GD057J-LW-ACN] for your company, we will explain the specifications of the Renewal product that we are proposing this time below.

Specification comparison list		T-55265GD057J-LW-ACN [Current product]	T-55265GD057J-LW-AKN [New product]
TFT	Supplier	A	B
	Location	Taiwan	Taiwan
Polarizer		C	D
Driver IC		E	F
PCB	Board	G	H
	Assembly	I	Kyocera China factory
	Mounting components	For stable supply in the future, we will downsize capacitors and resistors, and change parts that have an old release time and are uncertain about supply to the latest parts.	
FPC		J	K
Backlight		L	Kyocera China factory
Touch panel		M	←

Specification comparison list		T-55265GD057J-LW-ACN [Current product]	T-55265GD057J-LW-AKN [New product]
Product Specifications	Polarizer	AG Coating	Glare treatment
	Interface	CMOS	←
	Outline dimensions	144.0(W)x104.6(H)x14.4(D) mm Projection not included.	144.0(W)x104.8(H)x14.4(D) mm Projection not included.
	Base color	Normally White	←
	Operating temperature	-20~70℃	←
	Storage temperature	-30~80℃	←
	Supply voltage (LCD)	Typ. 3.3V	←
	Current consumption (LCD)	Typ. 100mA	Typ. 60mA
	LED Forward current	60mA/Line	60mA/Line
	LED Forward voltage	Typ. 13.2V/Line (IF=60mA, Ta=25℃)	Typ. 11.6V/Line (IF=60mA, Ta=25℃)
	Operating life time	-	Typ. 70,000hr (IF=60mA, Ta=25℃)

New product outline dimensions



The MDL has an outer shape of 144.0mm x 104.6mm, but since there are four 0.1mm protrusions on the top and bottom, the height dimension is described as 104.8mm.

Electrical Specifications

AC Characteristic

Parameter	Symbol	Min.	Typ.	Max.	Units
CK Frequency	1/tosc	-	6.4	-	MHz
CK Period	tosc	-	156	-	ns
CK High Pulse Width Time	toscH	12	-	-	ns
CK Low Pulse Width Time	toscL	12	-	-	ns
CK Pulse Duty ratio	toscH/ tosc	-	50	-	%
Data Setup time	t _{ds}	12	-	-	ns
Data Hold time	t _{dh}	12	-	-	ns
HSYC (Horizontal Sync.) Signal Cycle	TH	-	62.8	-	μs
		-	408	450	clk
HSYC Pulse Width	TH _s	5	30	-	clk
Horizontal Display Term	TH _d	-	320	-	clk
ENAB Setup Time	t _{ens}	12	-	-	ns
ENAB Hold Time	t _{enh}	12	-	-	ns
VSYN (Vertical Sync.) Signal Cycle	TV	-	262	350	Line
VSYN Pulse Width	TV _s	1	3	5	Line
Vertical Display Term	TV _d	-	240	-	Line
Vertical Display Start	TV _{ds}	-	18	-	Line
HSYC-ENAB Phase Difference	THE	-	68	-	clk
HSYC-CK Phase Difference	THC	12	-	-	ns
HSYC-VSYN Phase Difference	THV	1	-	-	clk

The timing diagram illustrates the relationship between several signals: HSYN (Horizontal Sync), ENAB (Enable), CK (Clock), and VSYN (Vertical Sync). It shows the setup and hold times for data (RD, RD, GO, GS, BO, BS) relative to the clock and sync signals. Key parameters labeled include TH_s, TH_d, TH, TV_s, TV_d, TV, t_{ens}, t_{enh}, t_{ds}, t_{dh}, t_{oscH}, t_{oscL}, t_{osc}, and t_{osc}. The diagram also shows the phase differences between HSYN-ENAB, HSYN-CK, and HSYN-VSYN.

T-55265GD057J-LW-AKN [New product]							
Electrical Specifications	AC Characteristic	Item	Symbol	Min	Typ	Max	Note
		Clock	Frequency	1/Tc	—	6.3	7.0 MHz 2)
			Duty ratio	Tch/Tc	40	50	60 %
		Data	Set up time	Tds	5	—	ns
			Hold time	Tdh	10	—	ns
		Horizontal sync. signal	Cycle	TH	50.0	63.6	— μ s
			Pulse width	THp	2	96	200 clock
		Vertical sync. signal	Cycle	TV	251	262	280 line
			Pulse width	TVp	2	—	34 line
		Horizontal display period		THd	320		clock
		Hsync, Clock phase difference		THc	10	—	Tc 10 ns
		Hsync Vsync. phase difference		TVh	Tc	—	TH THp ns
		Vertical sync. signal start position		TVs	7		line
		Vertical display period		TVd	240		line
		Item	Symbol	Min	Typ	Max	Note
		Enable signal	Set up time	Tes	5	—	Tc 10 ns
			Pulse width	Tep	2	320	TH 10 clock
		Hsync – Enable signal phase difference		The	2	—	TH 340 clock

Specification comparison list			T-55265GD057J-LW-ACN [Current product]	T-55265GD057J-LW-AKN [New product]
Optical specifications (Typ.)	Response time	Rise	21ms	10ms
		Down	10ms	25ms
	Brightness (Center of display)		400cd/m ²	←
	Contrast ratio		350	←
	Viewing angle range CR>5	Upper	70deg	←
		Lower	70deg	←
		Left	70deg	←
		Right	70deg	←
	Optimal viewing angle		6 o'clock	←
	Chromaticity coordinates	Red	0.6209, 0.3652	0.625, 0.360
		Green	0.3319, 0.5657	0.330, 0.605
		Blue	0.1401, 0.0863	0.150, 0.090
		White	0.2919, 0.3040	0.305, 0.335

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LCD Interface	Connector	08-6260-033-340-829+	IMSA-9632S-33Z02-GFN4																																																																																																					
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LED Interface	Connector	CN2 : SHLP-06V-S-B (JST)	←																					
	Pin assignment	CN2	←																					
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Touch panel	Pin assignment	CN3	←																					
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Rev	Mark	Name	Date	Contents
00	None	M. Nakada	2022/8/2	Initial release
01				
02				
03				

THE NEW VALUE FRONTIER



KYOCERA Corporation