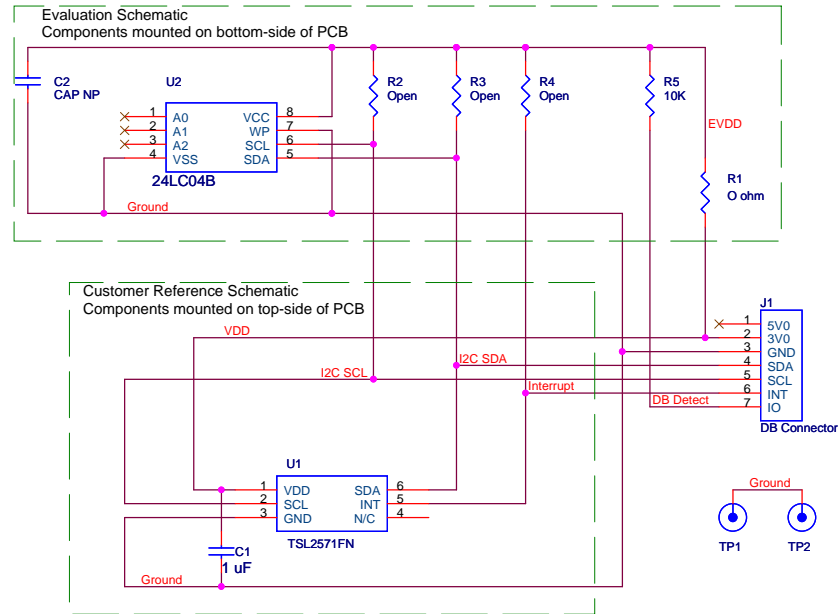


Notes:

- 4 layer PCB: 2 layers for Customer Reference and 2 layers for Evaluation.
- 20 mil trace (minimum) on all power and ground routes.
- Decoupling capacitors are physically close to U1 VDD and U2 VCC, respectively.
- Designed for TSL257x, and can be used with TCS347x.
- All passive components are EIA0603 except C1 which is EIA0402.
- Assembly drawing:
- PCB drawing:



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Title: **TSL:2571 Daughterboard**

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